

PDP-8

Synopsis

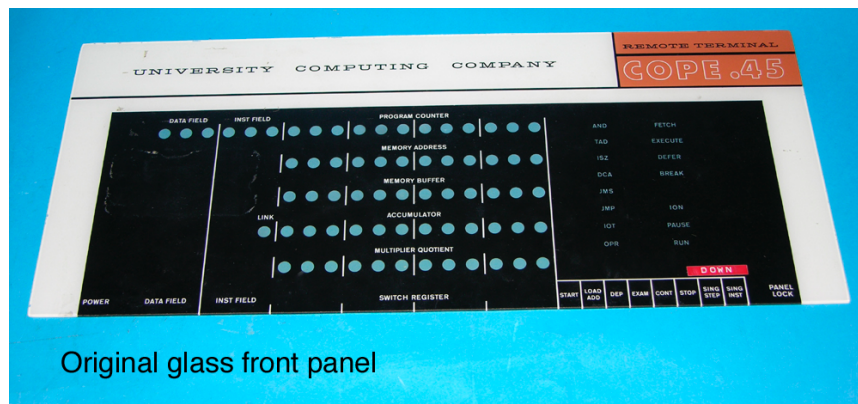
This is an auction of a private collection of vintage Digital Equipment Corporation (DEC) hardware, software and documentation. The computer in this auction is a DEC PDP-8 computer (aka "straight 8") originally introduced in 1965 as the first all transistorized minicomputer selling for under \$20,000 and designed by DEC engineer Edson DeCastro. The PDP-8 was the successor to the DEC PDP-5 computer. Historical records indicate that 1,450 straight 8s were manufactured and today only a handful are known to still exist.

Background

This particular PDP-8 appears to have been initially purchased by University Computing Co. of Texas as evidenced by the asset tag on the computer's frame:



The computer originally came with a customized silk screened glass front panel:



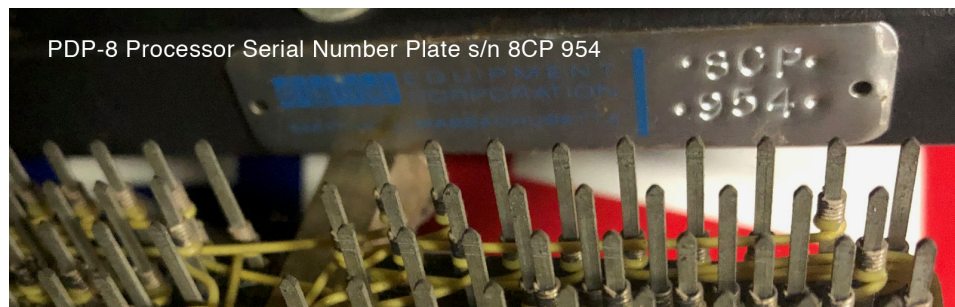
As can be seen on the original label, the unit was labeled "remote terminal" which implies that the computer may have been used as a remote terminal concentrator as UCC provided contract computer services in the 1960s.

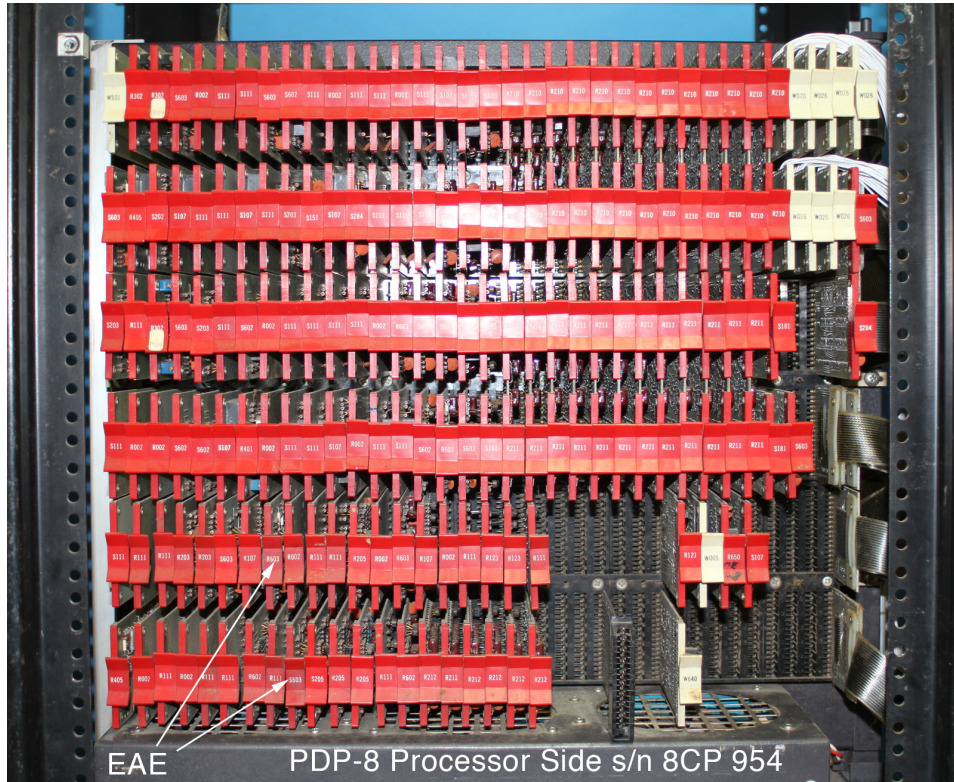
The computer at auction was purchased by the owner in 1975 when the owner was an undergraduate attending college at the dawn of personal computing. The PDP-8 system includes an ASR-33 teletype hardcopy terminal as well as additional racks of hardware. The PDP-8 utilizes diode

transistor logic (DTL) implemented on printed circuit boards known as “FlipChips”, DEC’s name for it logic modules. The PDP-8 is a 12 bit computer with 4096 12 bits of core (nonvolatile magnetic memory) with the capability of being expanded to 32K. In its heyday, the PDP-8 was used for real time data acquisition, typesetting and general purpose computing using languages such as BASIC, FOCAL, FORTRAN and ALGOL. Instructions and data can be toggled into the machine’s memory via the switches on the computer’s front panel. The computer was used as a hobby computer until the early 2000s when it was taken out of service when the owner needed to reduce his inventory of DEC hardware. There is a library of paper tape software included with the system (see catalog below) with over 290 individual tapes cataloged. Most of the tapes are DEC and DECUS (Digital Equipment Computer User Society) original tapes. There are also copies of original DEC/DECUS tapes included in the library as well as evolutionary updates to the diagnostic programs. The library includes hardware diagnostics, known as “MAINDECS” as well as languages, utility programs and system builder programs. Software documentation includes approximately 128 documents for programs. As a side note, there are MAINDECS and documentation for the later generation PDP-8/E computer included in the collection. Hardware documentation is comprised of manuals, including two copies the PDP-8 Maintenance Manual, engineering drawings, Users Handbook, Small Computer Handbook 66-67 First Edition, various editions of the DEC Logic Handbook describing the various FlipChips. There are 213 spare FlipChips of various types included in this auction. Three TU55 DECTape drives installed in a DEC H960 tall rack are included in the lot, albeit without a TC01 DECTape controller. Two additional H960 racks containing power distribution controllers as well as power supplies, a wire wrap backplane and an RK05 disk storage shelf are included. A DEC H967 heavy duty short rack is also included in the lot. As illustrated in several of the photographs, much of the documentation was obtained from the High Energy Physics group at Case Western Reserve University when they retired their PDP-8 systems in the early 1980s.

Hardware

DEC PDP-8 Computer in H960 rack CPU serial number 8CP 954:



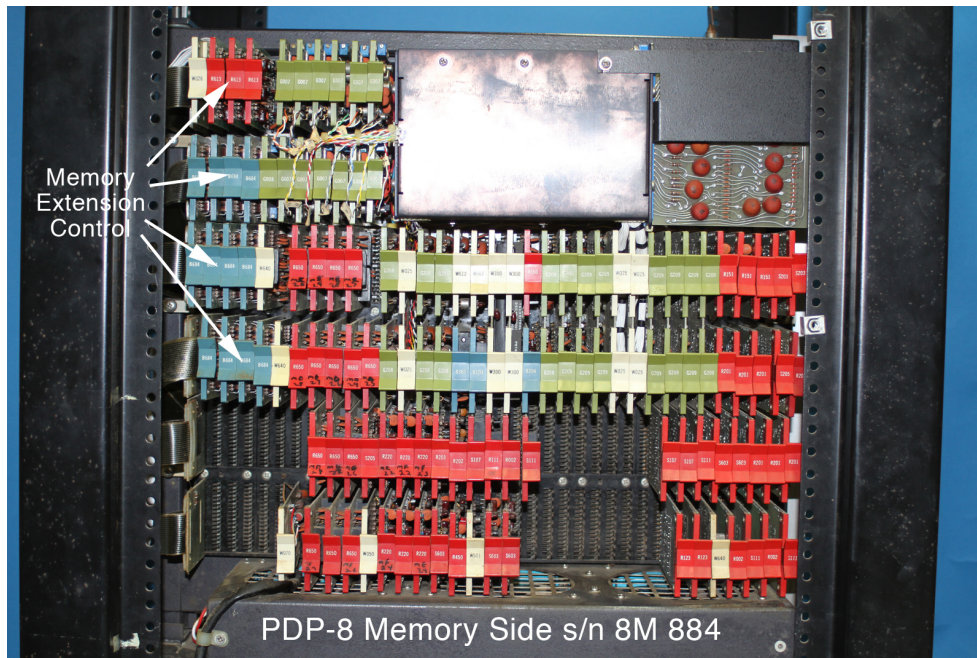


The PDP-8 processor includes the Type 182 EAE, Extended Arithmetic Element (hardware multiply / divide) as illustrated above.

Memory side serial number 8M 884:

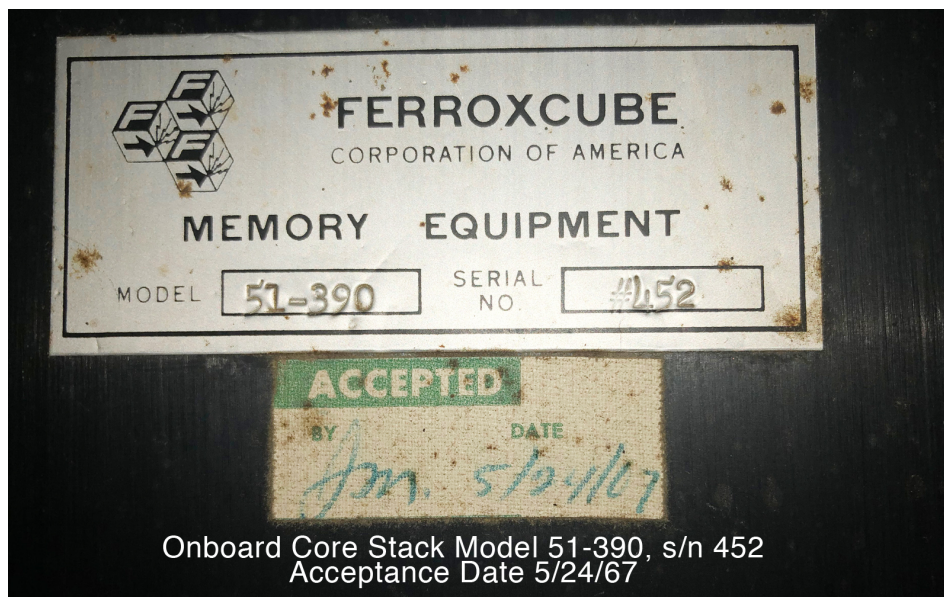


The memory side of the system is shown in the following photograph:



The system also includes a Type 183 Memory Extension control as shown above.

The onboard core stack illustrated in the previous picture is labeled:



The acceptance date on the core stack appears to read 5/24/67, which would make the time frame of manufacture during 1967. The system utilizes a 708 power supply:



Note Hours at 44,551

The power supply's hour meter is at 44,551 hours, which translates to slightly over five years of use.

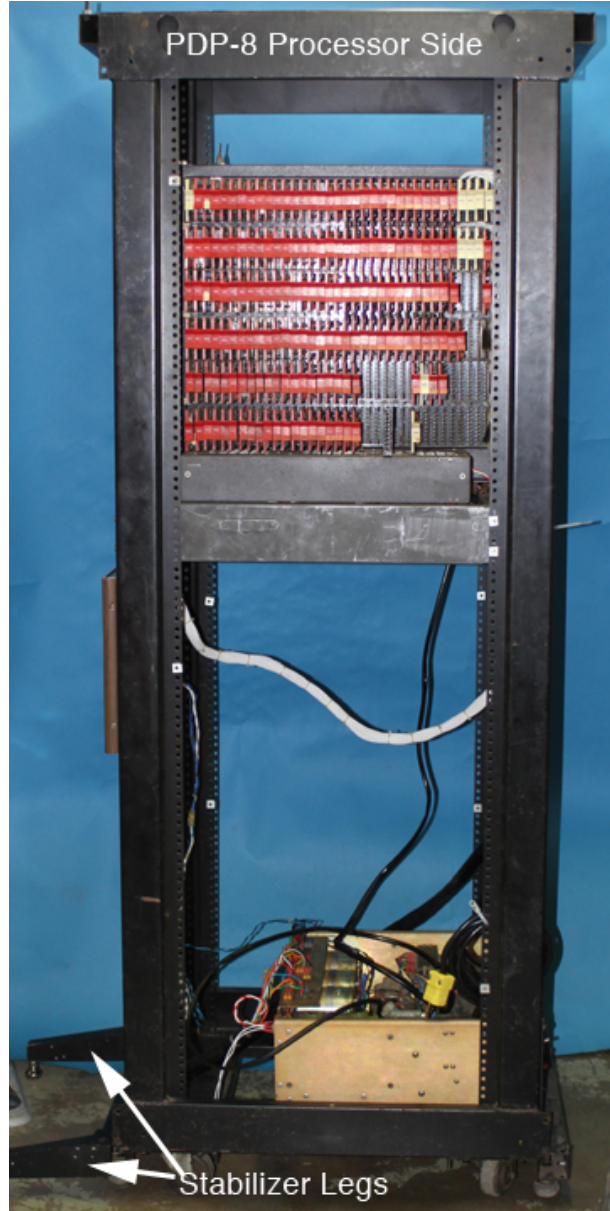
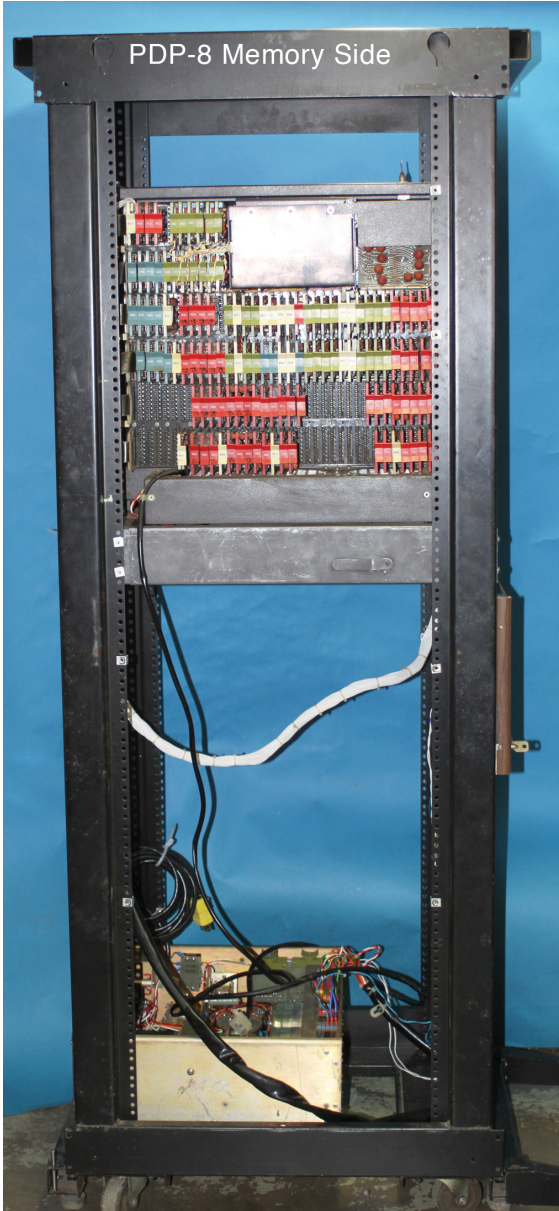
The PDP-8 has been installed in a DEC H960 rack that includes stabilizer legs.

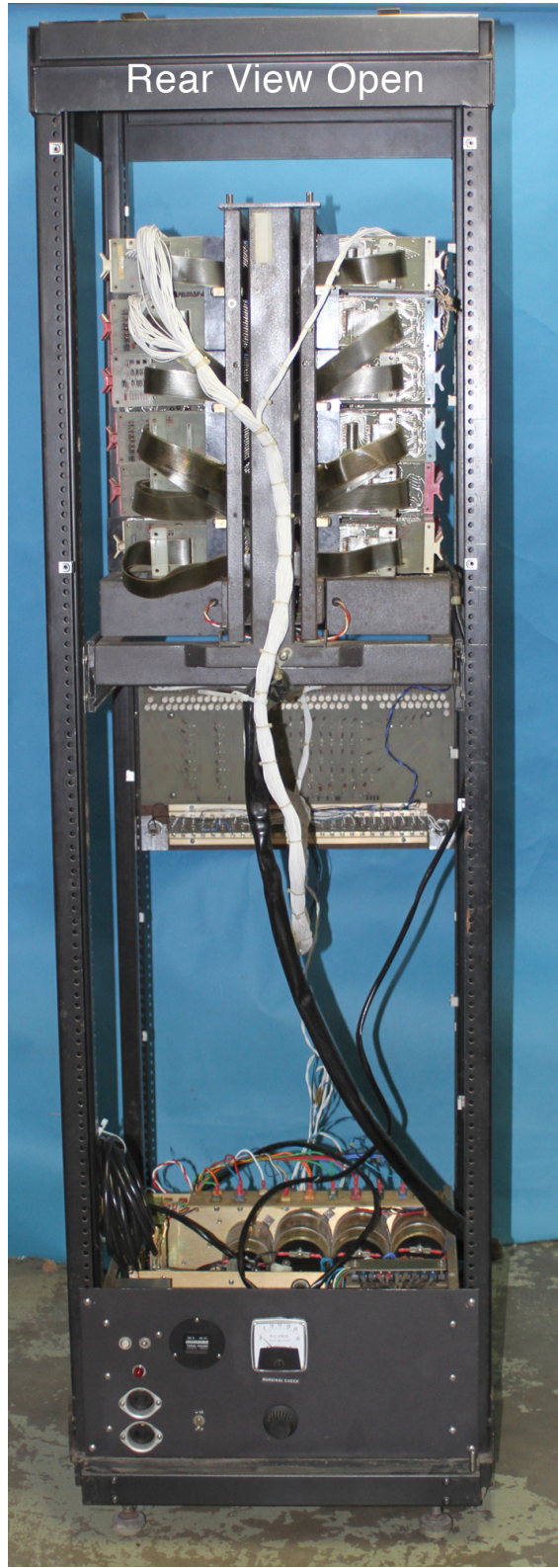
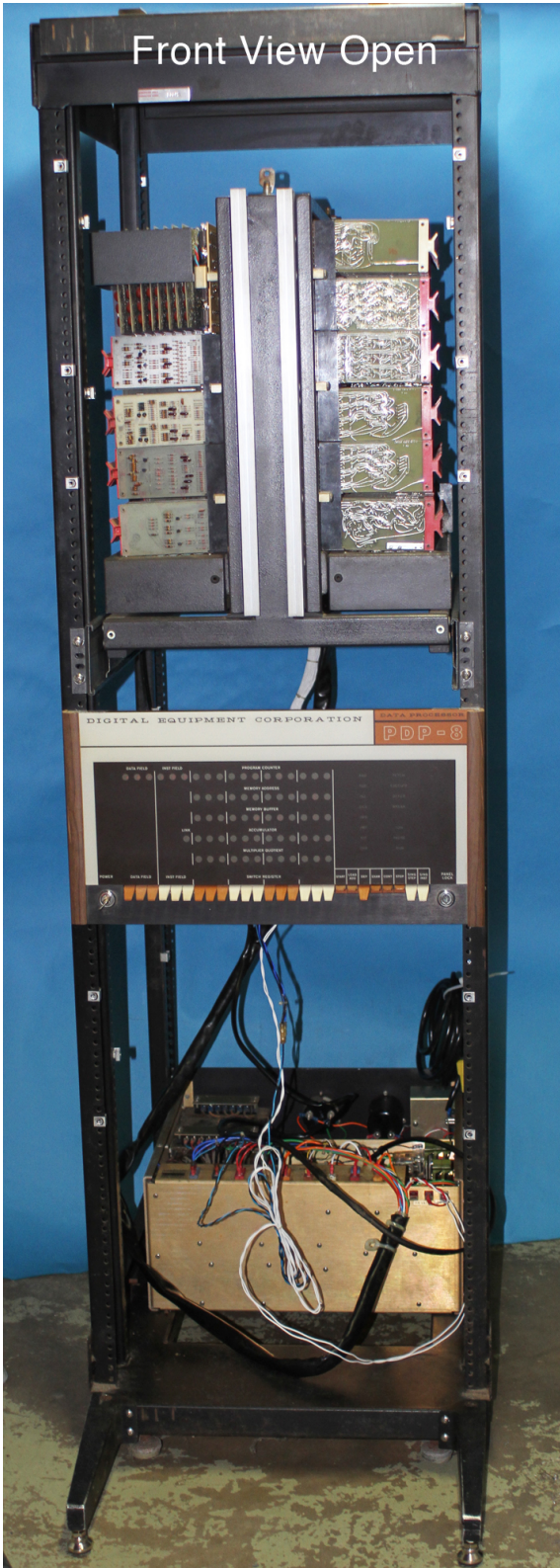




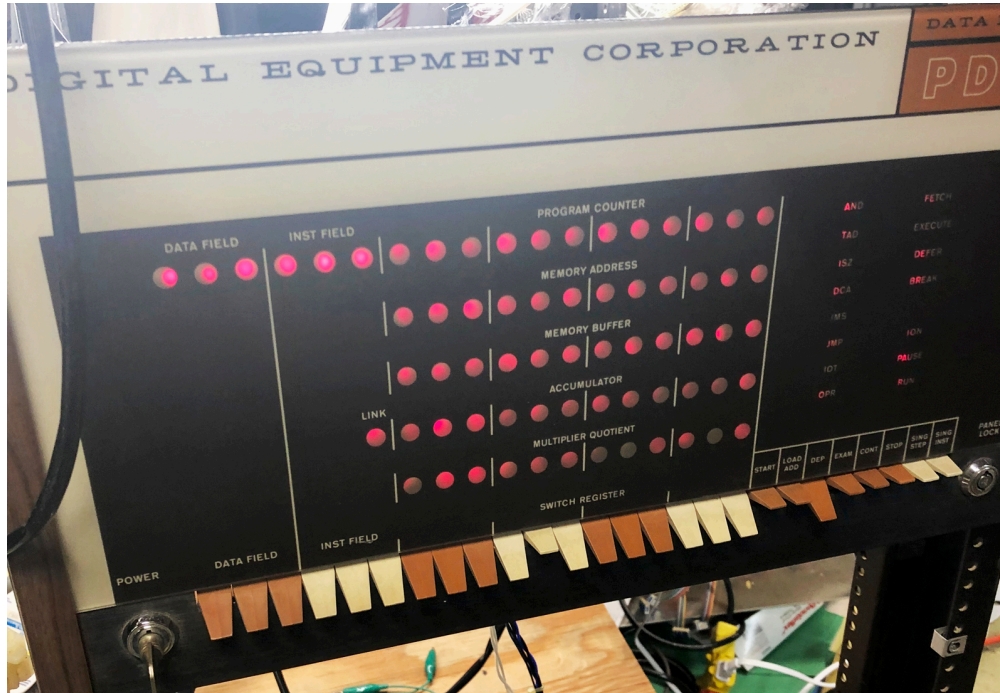
PDP-8 in H960 Rack Rear View

The following pictures show the rack without the side panels attached:





Full disclosure: The front panel of the PDP-8 was modified in the late '70s replacing the original tear drop incandescent lamps with LEDs.



The front panel is made of painted plastic. The original front panel was glass and apparently was used for an OEM application, as it does not bear DEC's name on it as illustrated earlier. When the system was reassembled after 20+ years in storage, there appear to be some issues. While the system powers up and appears to have some functionality, short programs toggled in from the console did not appear to run correctly. This problem may be attributed to the presence of the 183 Memory Extension Control without any additional memory present on the bus.

PDP-8 System in H960 Rack

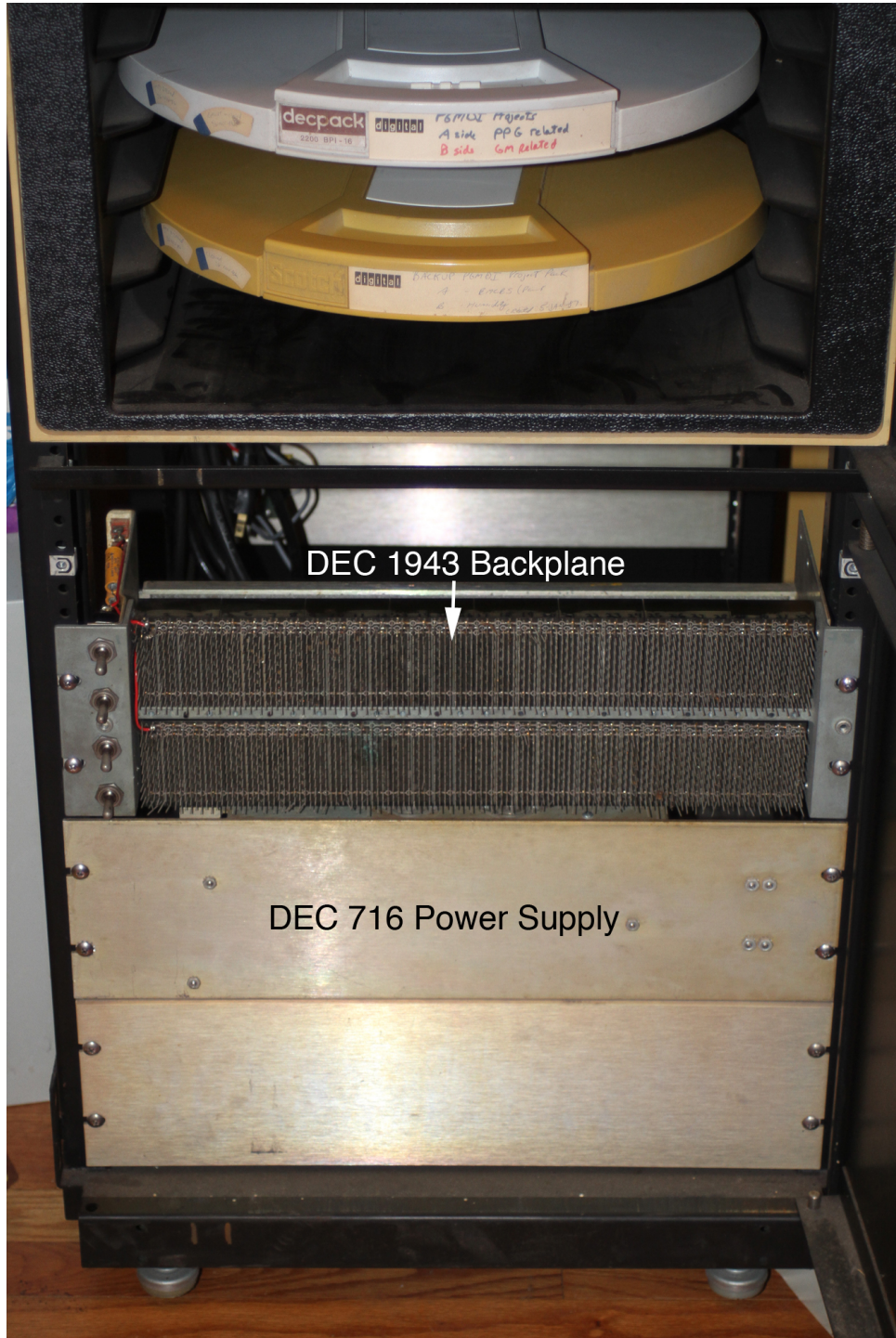
<u>Item</u>	<u>Description</u>	<u>Qty.</u>
PDP-8 System	PDP-8 Computer, 4K core memory includes Type 182 Extended Arithmetic Element (EAE), Type 183 Memory Extension Control, including two "Ace" keys PDP-8 Memory side serial number 8M 884 PDP-8 Processor side serial number 8CP 954 DEC 708 Power Supply (hour meter reads 44,551.4)	1
	Front Panel Assembly	1
	DEC H960 Heavy Duty (72") Rack consisting of:	1
	DEC H950-AA 72" Frame	1
	DEC H852-BA Stabilizer Legs (1pair) with new feet	1
	DEC H952-AA End Panel	2
	DEC H950-BA Door, Back, Right Hanging	1

DEC H960 Rack 1

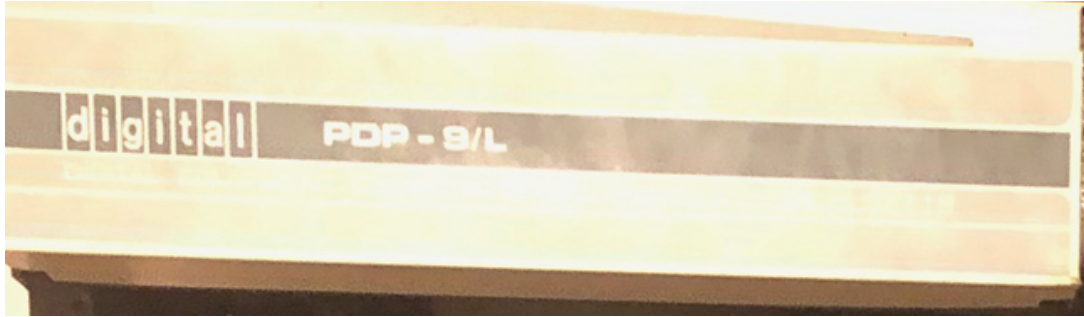
<u>Item</u>	<u>Description</u>	<u>Qty.</u>
H960	DEC H960 Heavy Duty (72") Rack consisting of:	1
	DEC H950-AA 72" Frame (designated Rack1)	
	DEC 861F Power Controller 120V/12A	1
	DEC H952-CA Fan Assembly 115 VAC	1
	DEC RKR01-AC/JB RK05 Cartridge Storage Unit	1
	DEC H950-HB Short Door, 22.75"	1
	DEC H950-BA Door, Back, Right Hanging	1
	DEC H950-DA Mounting Panel Door Frame (Right Hanging)	1
	DEC H960-LA Aluminum Logo Panel, Gray/Black Labeled	1
	PDP-9/L	
	DEC 12-09154-00 Mounting Slides, non tilting	3
	DEC 716 +5/-15VDC Power Supply (tested ok)	1
	DEC 1943 32 slot double sided dual height wire wrap backplane with 4 margin switches	1
	Power Distribution Rail mounted left side of rack	1

Photographs of Rack 1 and contents

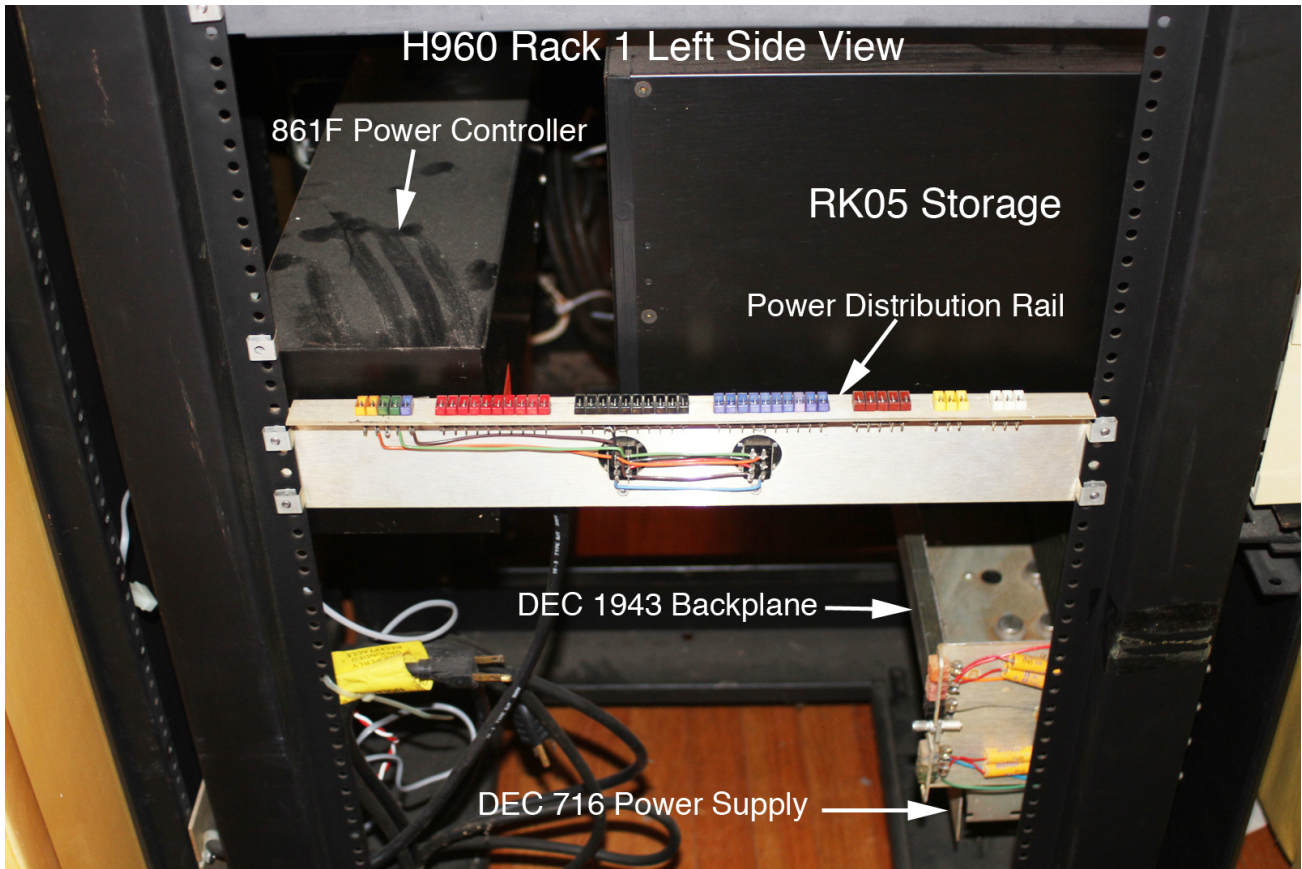




Close up of Rack 1.



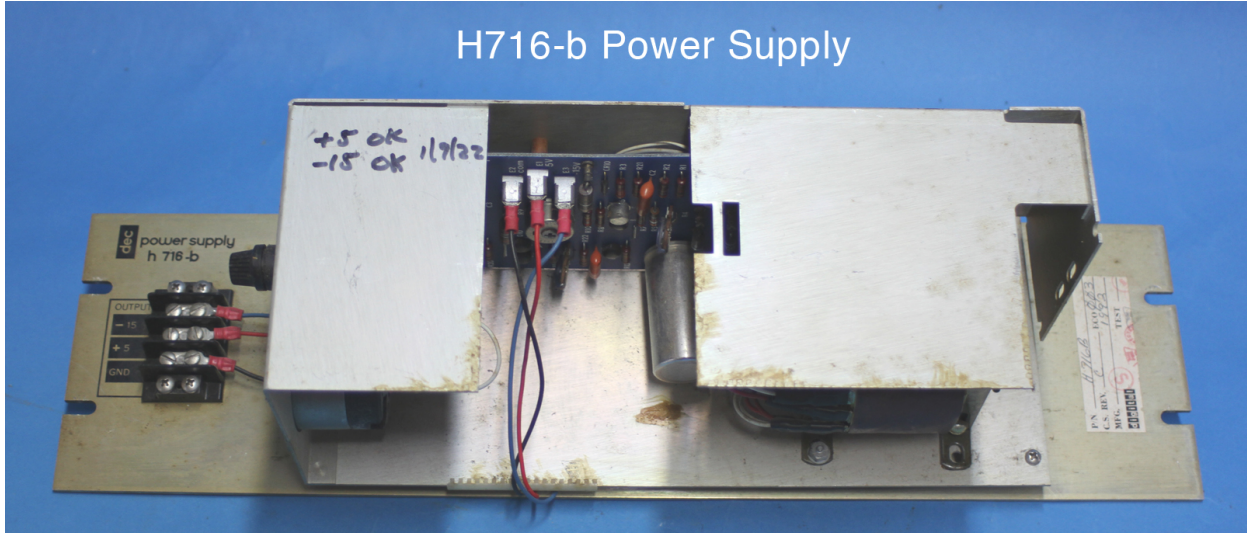
Rack 1 Bezel



Rack 1 Left Side View



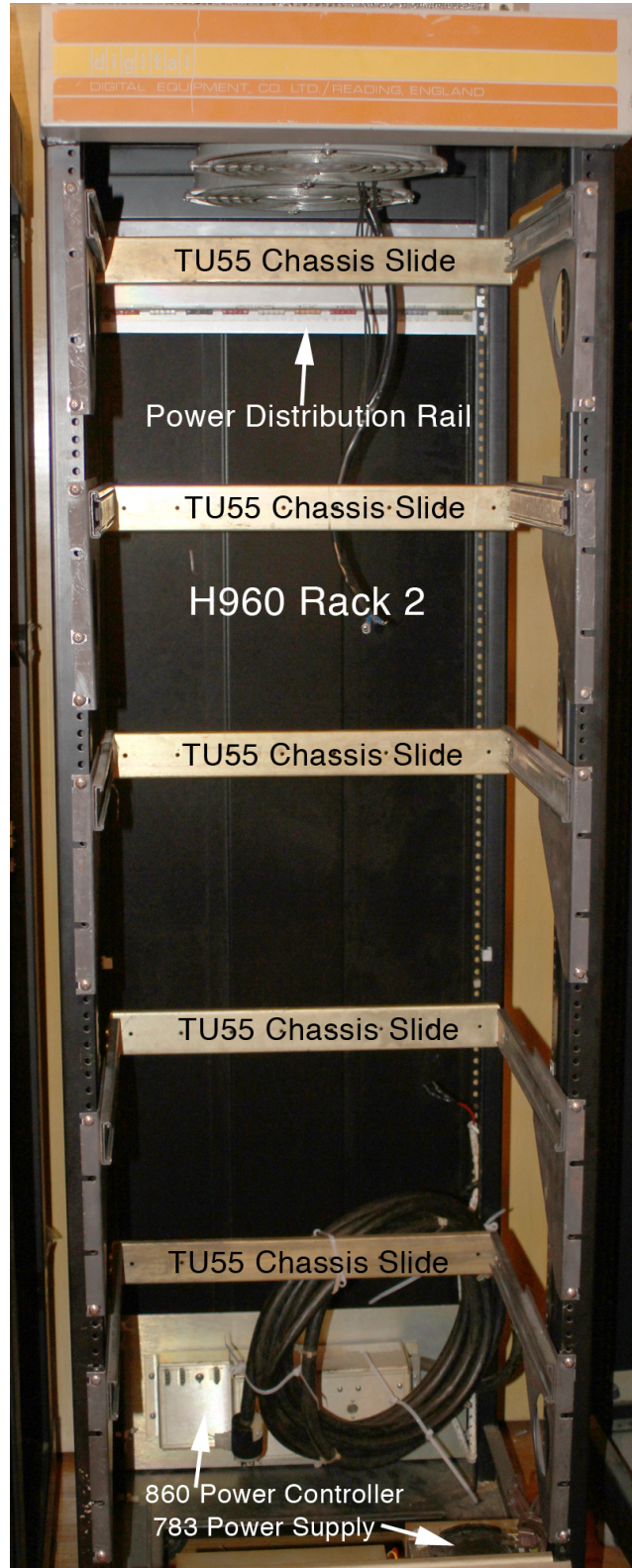
DEC 1943 Backplane



H960 Rack 2

<u>Item</u>	<u>Description</u>	<u>Qty.</u>
H960	DEC H960 Heavy Duty (72") Rack consisting of: DEC H950-AA 72" Frame (designated Rack 2) DEC 860 Power Controller untested	1 1
	DEC H952-CA Fan Assembly 115 VAC	2
	DEC H960-LA Aluminum Logo Panel, Orange/Yellow, labeled "Digital Equipment Co., Ltd/Reading, England"	1
	DEC TU55 Chassis Slides	5
	DEC H950-BA Door, Back, Right Hanging	1
	DEC 783 +10/-15 VDC Power Supply (tested ok)	1
	DEC H952-BA Stabilizer Legs (2 right legs, no feet)	2
	Power Distribution Rail mounted rear top	1

Photographs of Rack 2 and contents





Rack 2 Bezel



860 Power Controller



Power Distribution Rail

DEC H960 Rack 3

<u>Item</u>	<u>Description</u>	<u>Qty.</u>
H960	DEC H960 Heavy Duty (72") Rack consisting of:	1
	DEC H950-AA 72" Frame (designated Rack 2)	
	DEC 860 Power Controller untested	1
	DEC H952-CA Fan Assembly 115 VAC	2
	DEC H960-LA Aluminum Logo Panel, Yellow/Orange (PDP-8/E style)	1
	DEC TU55 DEctape drives, serial numbers 878, 449, 2332	3
	DEC H950-BA Door, Back, Right Hanging	1
	DEC 728 +10/-15 VDC Power Supply (tested ok)	1
	DEC TU55 Chassis Slides	1
	Power Distribution Rail mounted rear top	1
	DEC H952-GA Aluminum Filler Strips (attached)	1
	Non-DEC filtered rack blower at base of rack	1

DEC TU55 DEctape drives

Two of the drives use the original color scheme of the brushed aluminum bar across the access panel on the right hand side. The top drive has serial number of 878. The middle drive has a serial number of 449. The bottom drive has the more common black and white color scheme seen in later generation DEC peripherals and has serial number 2332.

Photographs of Rack 3 and contents



Close up of TU55 DECTapes





In addition to the H960 racks, the collection includes an H967 heavy duty short rack consisting of the following:

<u>Item</u>	<u>Description</u>	<u>Qty.</u>
H967	DEC H967 Heavy Duty Short (50") Rack consisting of:	1
	DEC H957-AA 50" Frame	1
	DEC 861C Power Controller 120V/24A (tested ok)	1
	DEC H957-HA Fan Assembly 115 VAC	2
	DEC H957-DA Mounting Panel Door Frame, Right Hanging	1
	DEC H957-BA Full Rear Door, Right Hanging	1
	DEC H957-FA End Panel Right Hanging	1
	DEC H957-FB End Panel Left Hanging	1

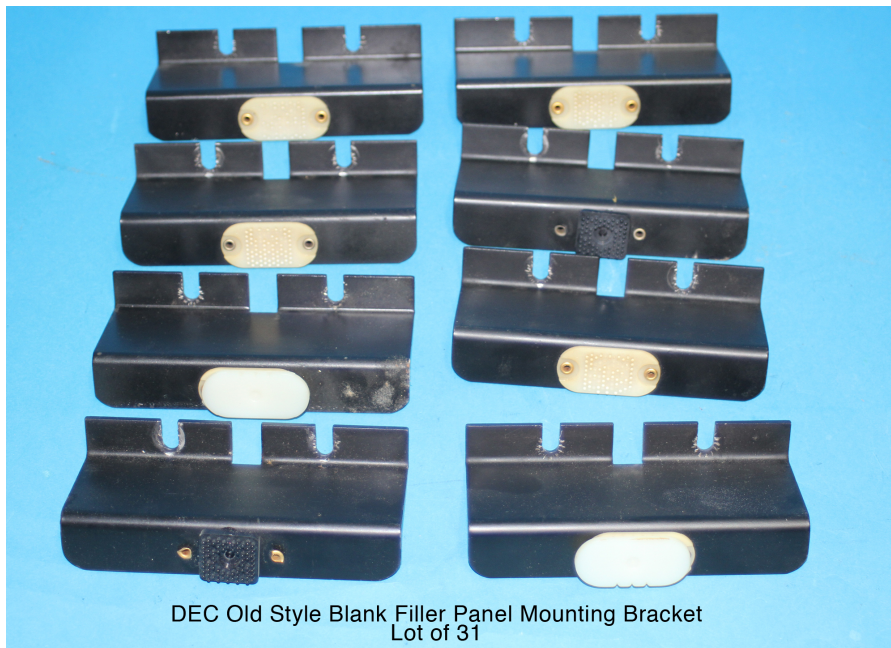
Photographs of H967 Short Rack

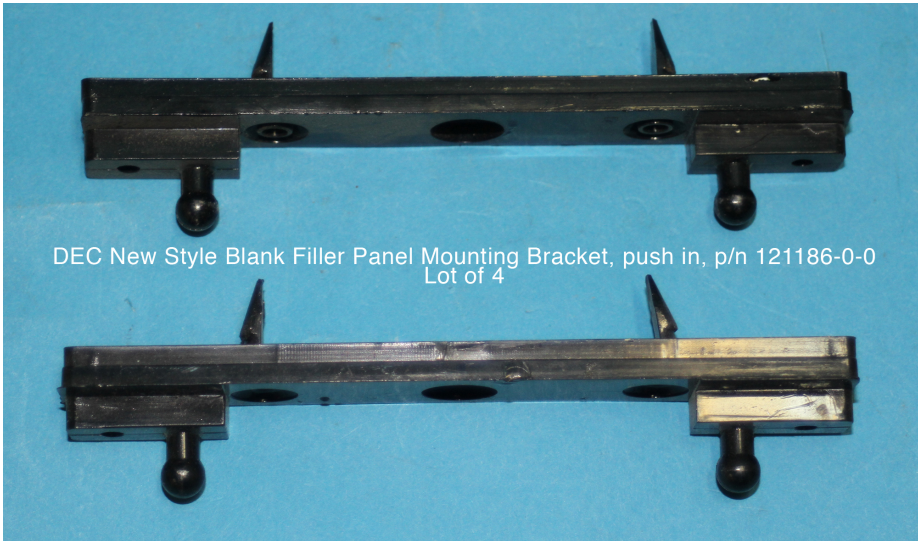
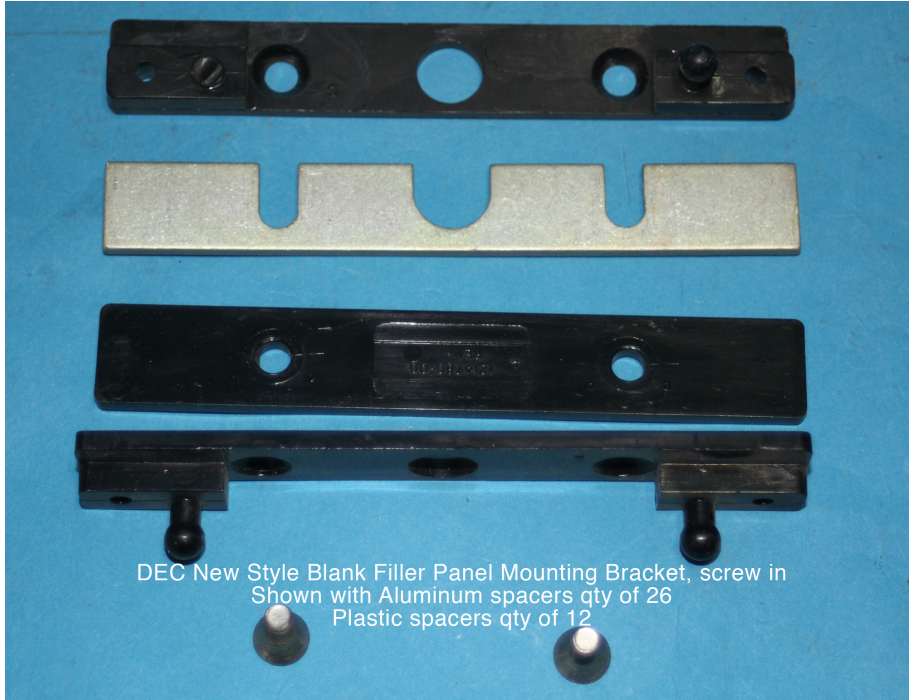


Spare Parts

This auction also contains numerous spare parts including FlipChip printed circuit cards and rack accessories.

<u>DEC Part No.</u>	<u>Description</u>	<u>Qty.</u>
Unknown	DEC Old Style Blank Filler Panel Mounting Bracket	31
Unknown	Spacer, aluminum for DEC new style Panel Mounting Bracket	26
1247800-00	Spacer, plastic for DEC new style Panel Mounting Bracket	12
121186-0-0	DEC New Style Blank Filler Panel Mounting Bracket, push in	4
Unknown	DEC New Style Blank Filler Panel Mounting Bracket, screw in (some broken mounting nubs)	53
H950-PA	DEC Blank Bezel Cover Panel 5.25"	3
H950-QA	DEC Blank Bezel Cover Panel 10.5"	16
Unknown	DEC Old Style Blank Bezel Cover Panel, 5.25"	24
Unknown	Miscellaneous Brackets	lot
Unknown	DECtape Storage Rack Table Top	1



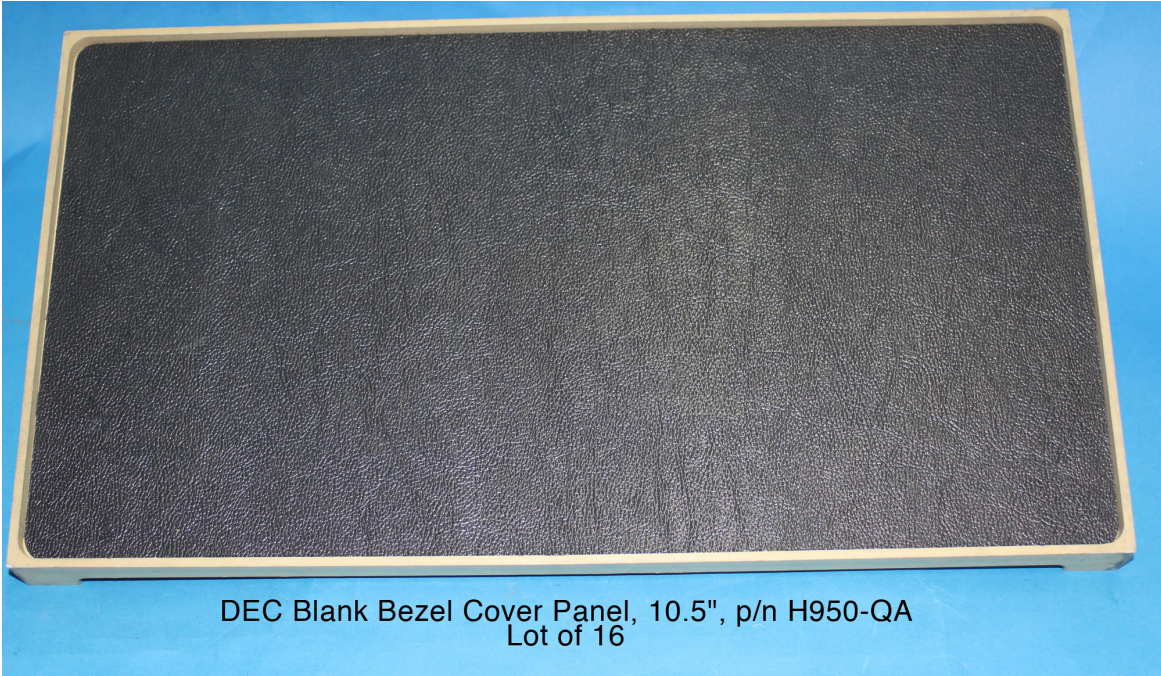




DEC New Style Blank Filler Panel Mounting Bracket, screw in
Lot of 53



DEC Blank Bezel Cover Panel, 5.25", p/n H950-HA
Lot of 3



DEC Blank Bezel Cover Panel, 10.5", p/n H950-QA
Lot of 16



DEC Old Style Blank Bezel Cover Panel, 5.25"
Lot of 24

It should be noted that these old style cover panels were used were used initially on the PDP-9 family of computers as illustrated in this sales brochure:

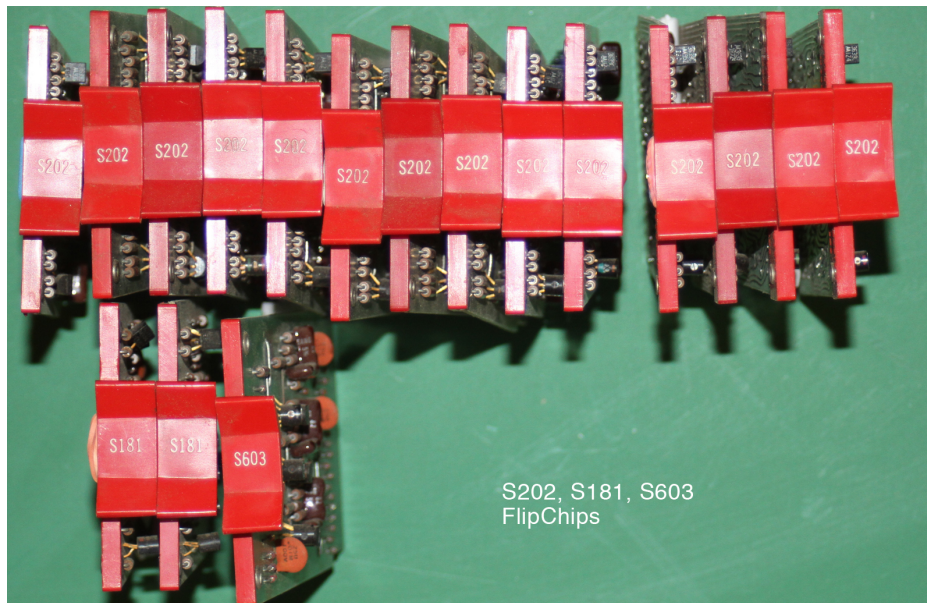
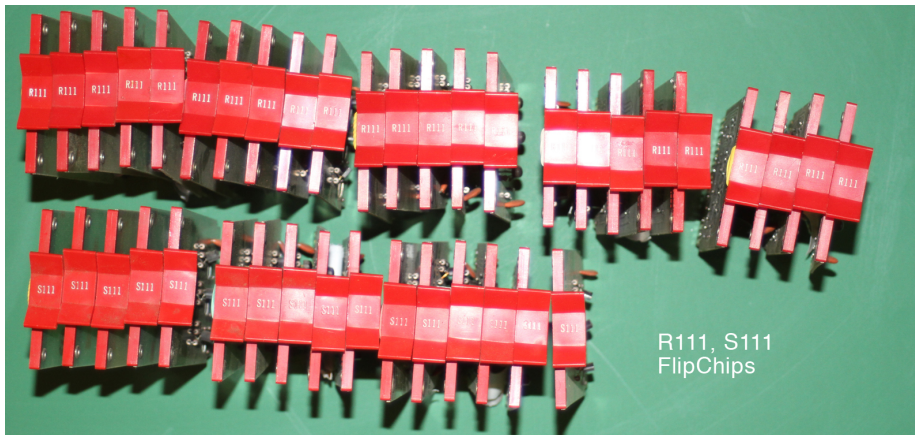
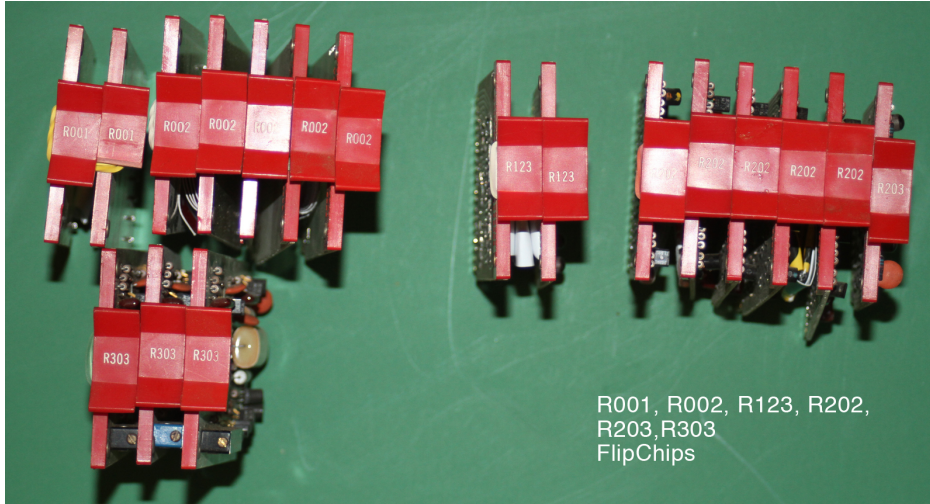


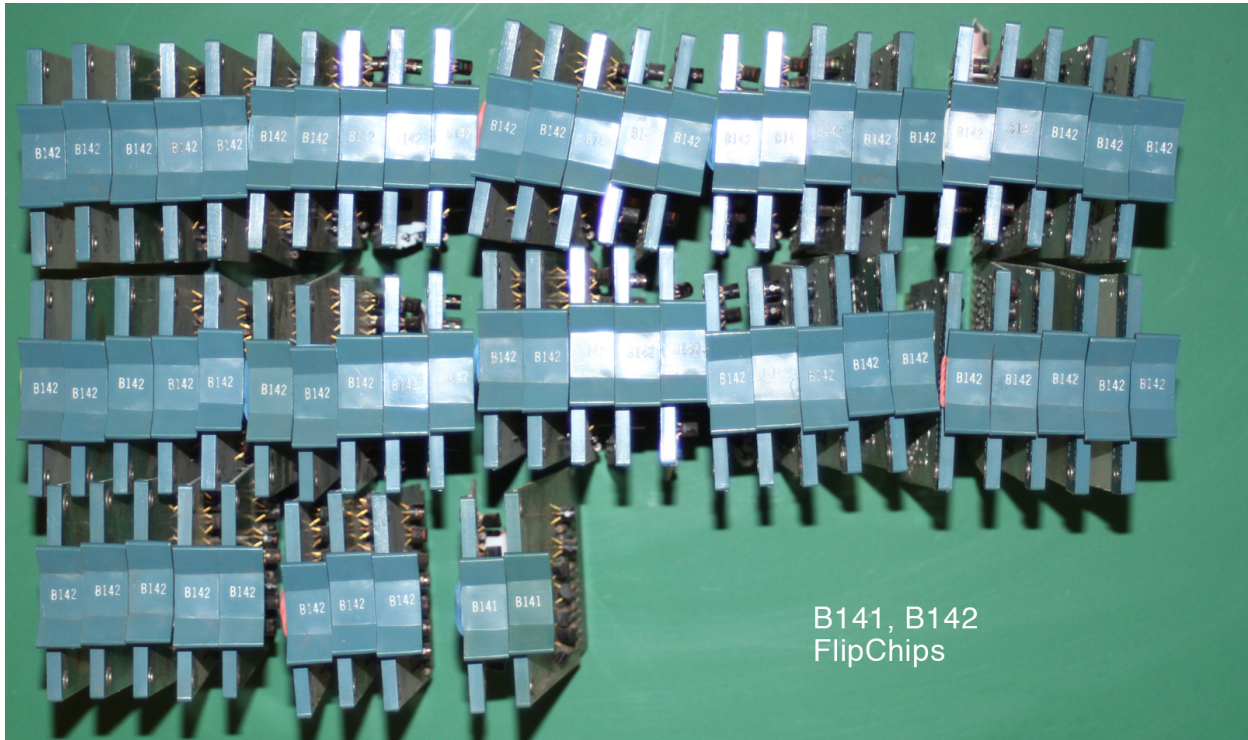
These brackets were found mixed in with the bezel mounting brackets



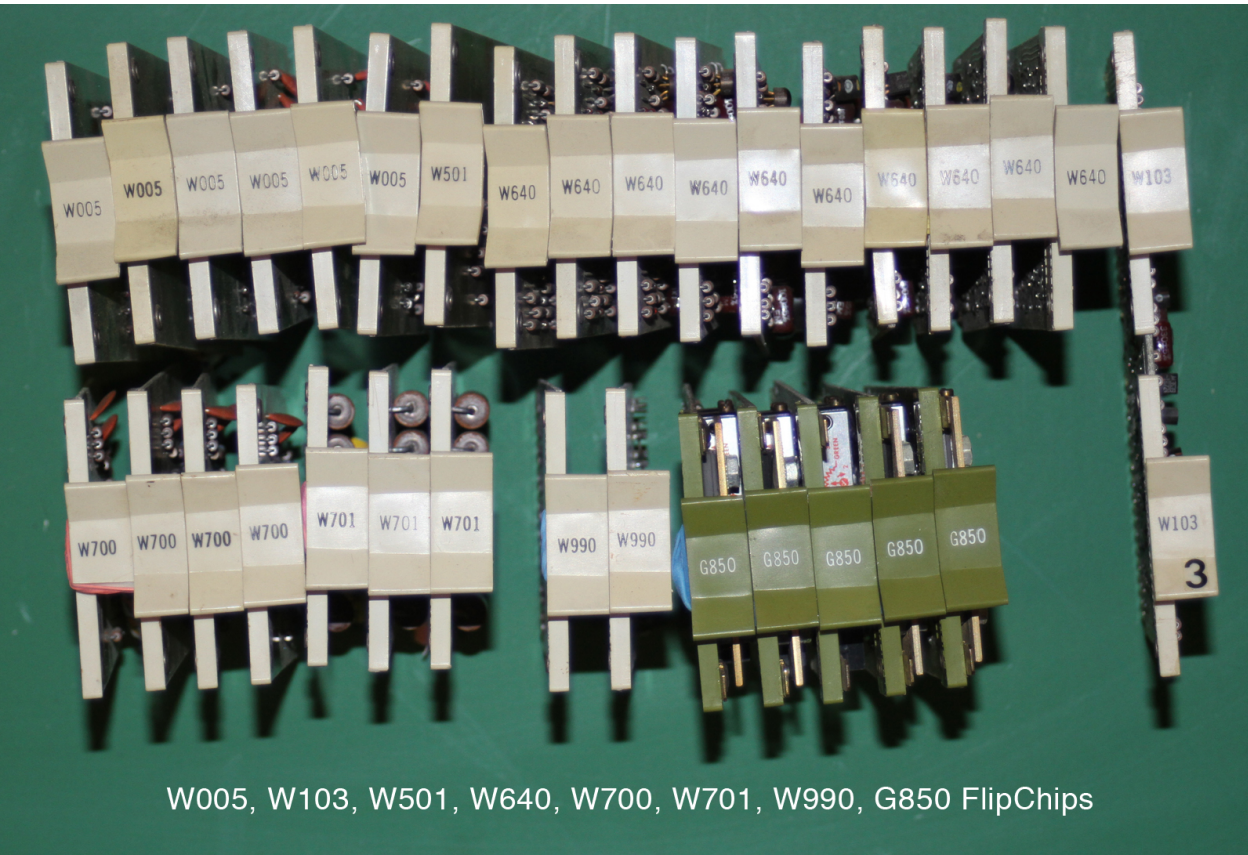
<u>Part Number</u>	<u>Spare FlipChip Boards</u> <u>Description</u>	<u>Qty.</u>
R001	Diode Network	2
R002	Diode Cluster	5
R107	Inverter	8
R111	Diode Gate	24
R123	Input Bus Gate	2
R202	Flip Flop	5
R203	Triple Flip Flop	1
R303	Integrating One Shot	3
S107	Inverter	24
S111	Diode Gate	16
S181	DC Carry Chain	2
S202	Flip Flop	14
S603	Pulse Amplifier	1
B141	Diode Gate	2
B142	Diode Gate	58
W005	Clamped Load	6
W103	PDP-8 Device Selector	1
W501	Schmitt Trigger	1
W640	Pulse Amplifier	10
W700	Switch Filter	3
W701	Input Network	3
W990	Blank Module	2
G681	8 Track Diode Matrix (RF08 disk head assembly)	15
G850	TU55 DECTape Motor Driver	5

The total number of spare FlipChip modules is 213.

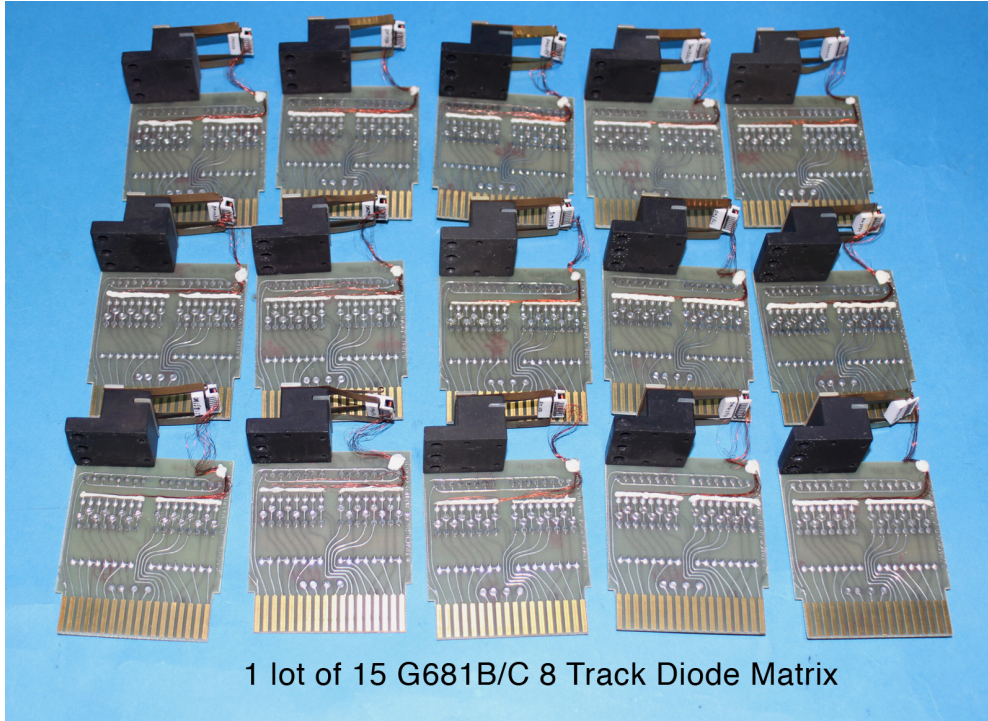




B141, B142 FlipChips

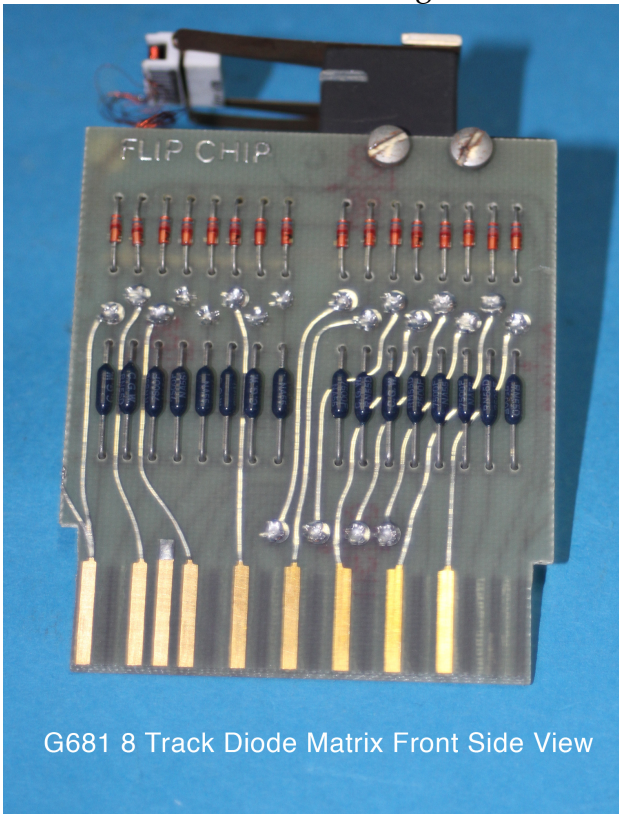


W005, W103, W501, W640, W700, W701, W990, G850 FlipChips

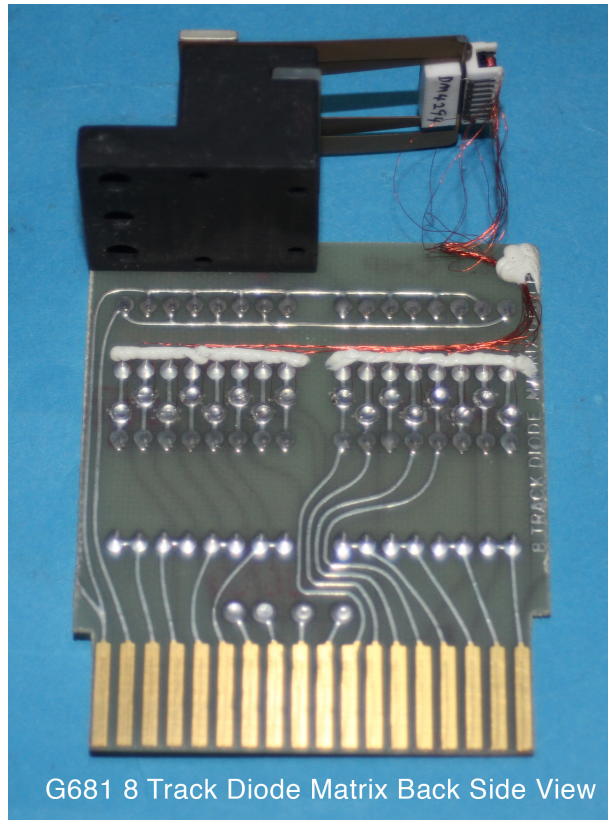


1 lot of 15 G681B/C 8 Track Diode Matrix

The G681 8 Track Diode Matrix FlipChips are actually 8 track disk head assemblies for the RF/RS08 and RF/RS09 262K disk storage units. Both revision B and C are included in this lot.

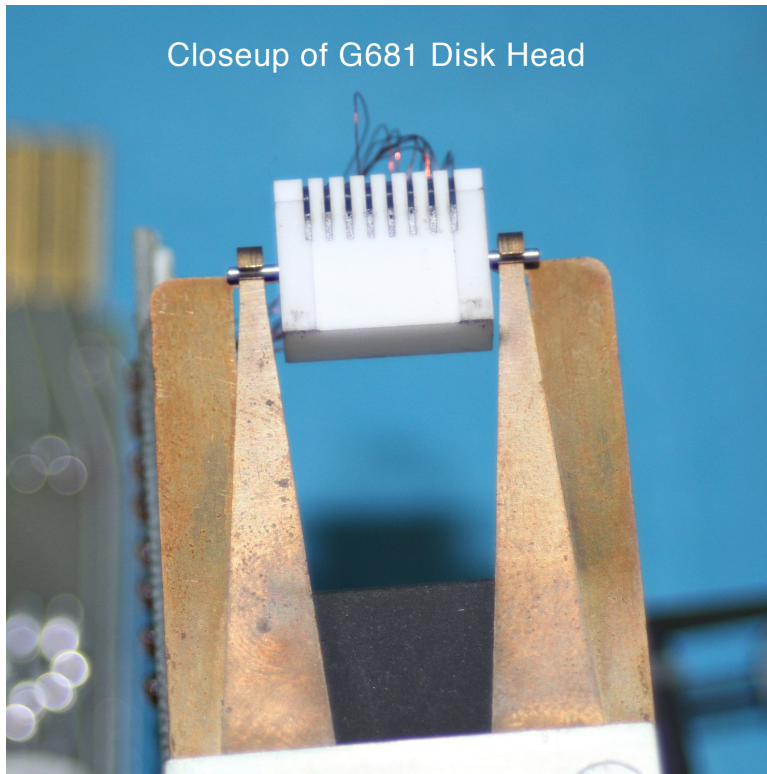


G681 8 Track Diode Matrix Front Side View



G681 8 Track Diode Matrix Back Side View

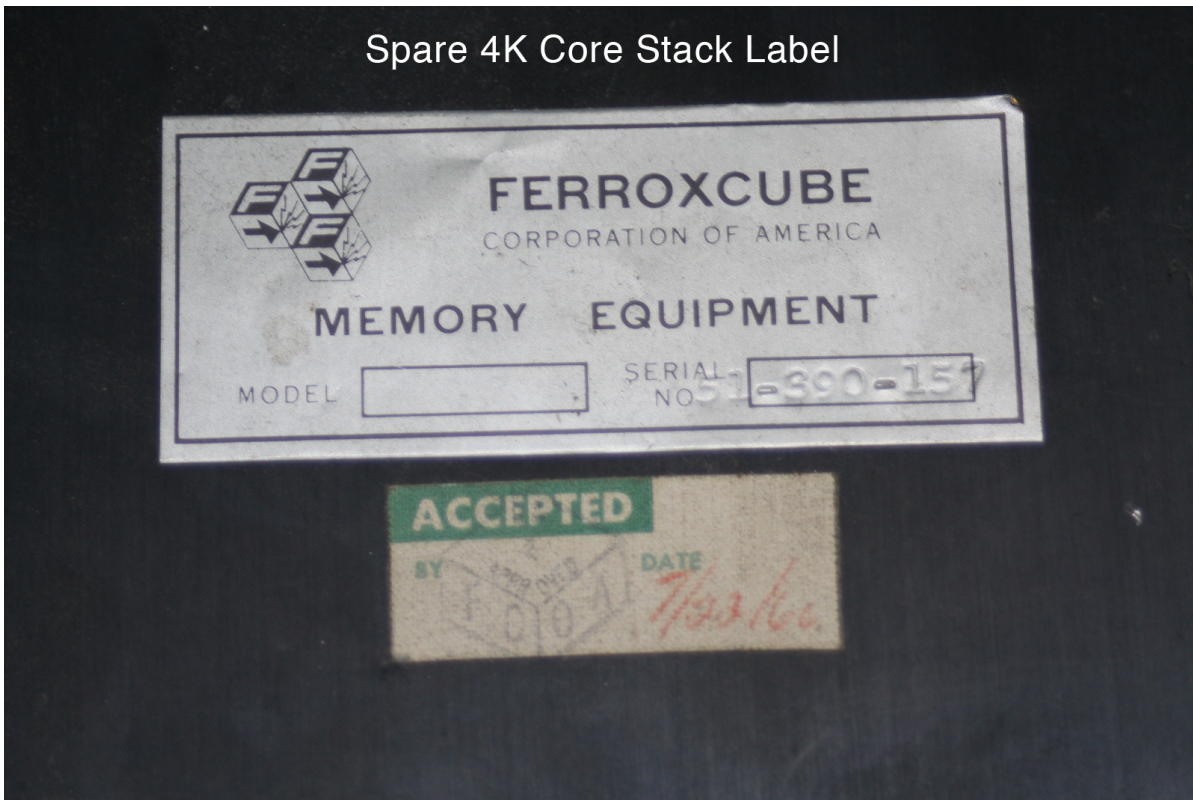
This is a close up of the disk head itself:



Additional spare parts include a lot of 6 PDP-8 cooling fans (untested) with one fan missing a blade as illustrated:



Also included is a spare Ferroxcube 4K core stack missing one of the sense amp boards:



The label on the spare core stack shows an acceptance date of what appears to be 7/23/66 with a serial number of 51-390-157.

ASR-33 TeleType

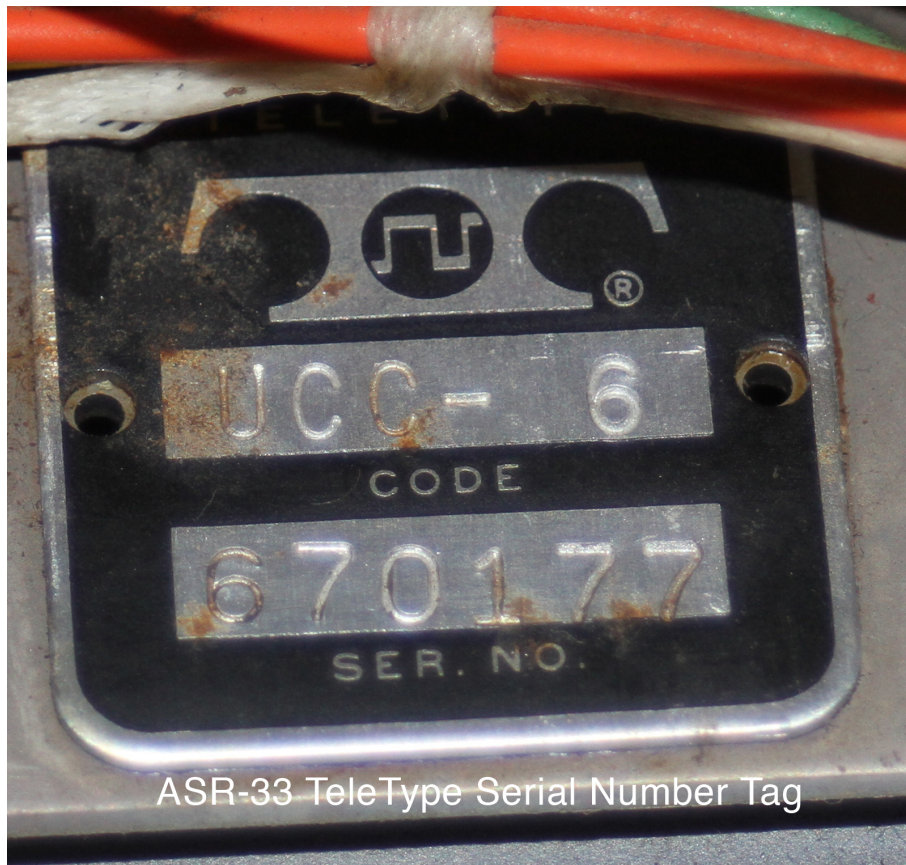
The ASR-33 TeleType included in this auction is illustrated here:





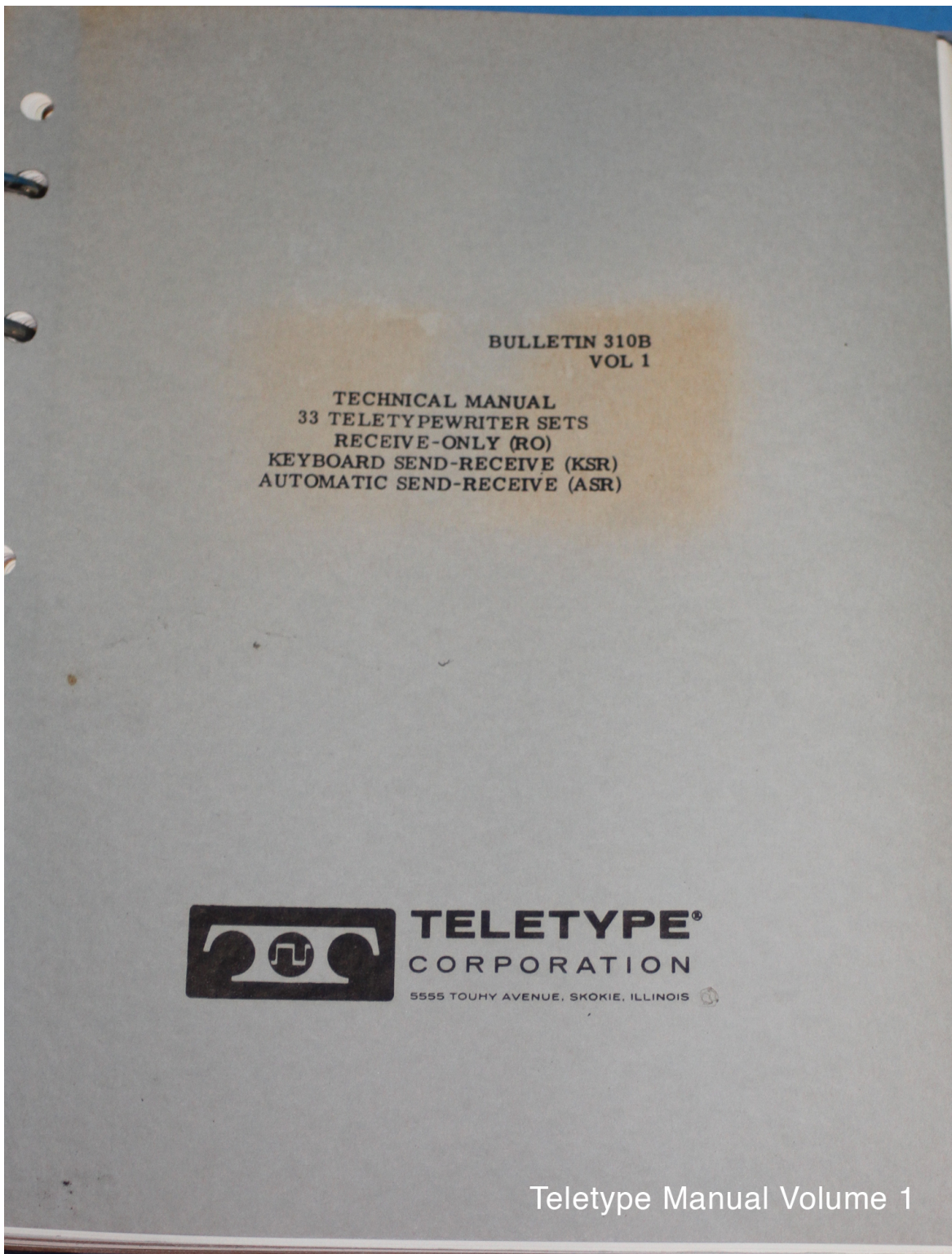
ASR-33 Teletype Rear View

As illustrated in the previous picture, the Teletype was purchased used from Newman Computer Exchange of Ann Arbor Michigan in the late '70s. The unit's serial number is 670177 as illustrated on the internal serial number tag:



ASR-33 Teletype Serial Number Tag

Included are the manuals purchased from TeleType Corporation, again in the late '70s.



BULLETIN 310B
VOL 2

TECHNICAL MANUAL
33 TELETYPEWRITER SETS
KEYBOARD SEND-RECEIVE (KSR)
RECEIVE-ONLY (RO)
AUTOMATIC SEND-RECEIVE (ASR)



TELETYPE[®]
CORPORATION
5555 TOUHY AVENUE, SKOKIE, ILLINOIS

Teletype Manual Volume 2

BULLETIN 1184B

33 PAGE PRINTER SET

(ASR, KSR AND RO)

PARTS



TELETYPE[®]
CORPORATION

5555 TOUHY AVENUE, SKOKIE, ILLINOIS

Teletype Manual Volume 3

Teletype was modified so that it could be switched between two 20mA current loop devices such as the computer and an acoustic coupler used for dial up timesharing.

Software

As stated earlier, the software collection is comprised of diagnostic software, known as MAINDECs, operating system software, language software, games and what is now known as application software. For some of the software there are multiple versions of varying release dates that provide a little bit of a history of the evolution of that particular piece of software. The bulk of the software is original DEC or DECUS paper tapes while there are also copies of originals and those are annotated as such. Multiple copies of some items also exist.

PDP-8 Paper Tape Software Catalog

	<u>Name</u>	<u>Part Number</u>	<u>Date</u>	<u>Type</u>	<u>No. Available</u>	<u>Notes</u>
1	PAL III Binary Format PDP-8	PA-1003 TP-1351	9/5/69	Original	1	Source unknown
2	Edit-8 Binary Format PDP-8	PA-1002 TP-1351	9/5/69	Original	1	Source unknown
3	Instruction Test Part 1	MAINDEC-8I-D01C-PB	12/10/69	Copy	1	
4	Instruction Test Part 2B	MAINDEC-8I-D02B-PB	1/3/68	Copy	1	
5	Instruction Test Part 2B	MAINDEC-8I-D02B-PB	1/3/68	DEC Original	1	
6	Instruction Test 2	MAINDEC-8I-D02B-PB	3/21/68	Copy	1	
7	Instruction Test 1	MAINDEC-8I-D01C-PB	12/10/69	Copy	1	
8	PDP 8E Memory Power On/Off Test	MAINDEC-8E-D1GA-PB	10/28/70	DEC Original	1	
9	PDP 8E Memory Power On/Off Test	MAINDEC-8E-D1GA-PB	10/28/70	Copy	1	
10	PDP 8 Memory Power On/Off Test	MAINDEC-8E-D1AC-PB	9/16/68	Copy	1	
11	PDP-8 Instruction Test Part 3A	MAINDEC-801-3A-PB	7/19/65	DEC Original	1	
12	PDP-8 Instruction Test Part 3A	MAINDEC-801-3A-PB	7/19/65	Copy	1	
13	Instruction Test Part 2B	MAINDEC-801-2B	3/22/66	DEC Original	1	
14	Instruction Test Part 1	MAINDEC-12-D0BA-PB	10/12/69	Copy	1	
15	DM01 Exerciser SA:0200	MAINDEC-08-D8SB-PB	1/6/69	Copy	1	
16	RF08 MultiDisk II (256K)	MAINDEC-08-D5FA-PB	3/26/69	Copy	1	
17	RF08 Disk Data (256K)	MAINDEC-08-D5EB-PB	10/3/69	Copy	1	
18	DF32 Multi Disk	MAINDEC-08-D5DB-PB	8/22/68	DEC Original	2 copies	
19	DF32 Multi Disk	MAINDEC-08-D5DB-PB	8/22/68	Copy	1	
20	DF32 Disk Data, Mini Disk, Interface, Address, Data Test	MAINDEC-08-D5CC-PB	4/4/68	DEC Original	1	
21	DF32 Disk Data, Mini Disk, Interface, Address, Data Test	MAINDEC-08-D5CC-PB	4/4/68	Copy	1	
22	DF32 Discless Logic Test	MAINDEC-08-D5BB-PB	11/3/67	DEC Original	2 copies	
23	DF32 Discless Logic Test	MAINDEC-08-D5BB-PB	11/3/67	Copy	1	
24	Dectrex TC01 Random Exerciser	MAINDEC-08-D3RA-PB	1/9/68	Copy	1	
25	TC01 Extended Memory Exerciser	MAINDEC-08-D3EB-PB	1/5/68	Copy	1	
26	TC01 Basic Exerciser	MAINDEC-08-D3BC-PB	10/13/69	Copy	1	
27	TC01 Basic Exerciser	MAINDEC-08-D3BC-PB	10/13/69	Copy	1	
28	Family of 8 ASR33/35 Teletype Test Part 2	MAINDEC-08-D2QD-PB	6/4/68	Copy	1	
29	Family of 8 ASR33/35 Teletype Test Part 1	MAINDEC-08-D2PE-PB	2/21/69	Copy	1	
30	Family of 8 High Speed Reader/Punch Test	MAINDEC-08-D2GF-PB	2/28/70	Copy	1	
31	Family of 8 High Speed Reader/Punch Test	MAINDEC-08-D2GE-PB	6/10/69	Copy	1	
32	PDP-8, 8/I Basic Memory Checkerboard (High)	MAINDEC-08-D1L2-PM	6/7/68	Copy	1	
33	Basic PDP-8 8L Memory Checkerboard Low	MAINDEC-08-D1L1-PM	2/27/68	DEC Original	1	
34	PDP-8, 8/I Basic Memory Checkerboard (Low)	MAINDEC-08-D1L1-PM	2/27/68	Copy	1	
35	PDP-8, 8/I Extended Memory Control Test	MAINDEC-08-D1GD-PB	7/27/70	Copy	1	
36	PDP-8, 8/I Extended Memory Control Test	MAINDEC-08-D1GB-PB	5/5/68	Copy	1	
37	PDP-8, 8/S, 8/I Extended Memory Control	MAINDEC-08-D1GB-PB	no date	Copy	1	
38	PDP-8, 8/I Extended Memory Checkerboard	MAINDEC-08-D1EB-PB	5/1/68	Copy	1	
39	PDP-8, 8/I Extended Memory Checkerboard	MAINDEC-08-D1EB-PB	7/27/70	Copy	1	
40	Memory Address Test (High)	MAINDEC-08-D1B2-PM	4/1/68	Copy	1	
41	Memory Address Test (High)	MAINDEC-08-D1B2-PM	4/1/68	DEC Original	1	
42	Memory Address Test (Low) RIM	MAINDEC-08-D1B1-PM	4/1/68	Copy	1	
43	Instruction Test Part 3B (EAE)	MAINDEC-08-D0BA-PB	9/1/65	DEC Original	1	
44	Random ISZ Test (labeled good)	MAINDEC-08-D07B-PB	12/28/67	Copy	1	
45	Random JMP-JMS Test	MAINDEC-08-D05B-PB	12/28/67	Copy	1	
46	Random JMP-JMS Test	MAINDEC-08-D05B-PB	12/28/67	Copy	1	
47	Memory Power On/Off Test Bin	MAINDEC 829	12/2/65	DEC Original	1	
48	LT08 Teleprinter Test	MAINDEC 828	1/26/66	DEC Original	1	
49	Teleprinter Test	MAINDEC 814 RIM	4/29/65	DEC Original	1	
50	TTY Reader Test	MAINDEC 810 RIM	4/12/65	DEC Original	1	
51	TTY Reader Test	MAINDEC 810 RIM	4/12/65	Copy	1	
52	Address Test	MAINDEC 803 RIM	5/11/65	DEC Original	1	
53	Address Test	MAINDEC 803 RIM	5/11/65	Copy	1	
54	Checkerboard High	MAINDEC 802 RIM	9/1/65	DEC Original	1	
55	Checkerboard High	MAINDEC 802 RIM	9/1/65	Copy	1	
56	Checkerboard Low	MAINDEC 802 RIM	9/1/65	DEC Original	1	
57	Checkerboard Low	MAINDEC 802 RIM	9/1/65	Copy	1	
58	JMS and JMP Test Binary	MAINDEC 801-2C	8/30/65	DEC Original	1	
59	JMS and JMP Test Binary	MAINDEC 801-2C	8/30/65	Copy	1	
60	Instruction Test Part1	MAINDEC 801-1	5/14/65	DEC Original	1	
61	Instruction Test Part1	MAINDEC 801-1	5/14/65	Copy	1	
62	Instruction Test Part 2A	MAINDEC -801-2A	3/10/66	DEC Original	1	
63	Instruction Test Part 2A	MAINDEC -801-2A	3/10/66	Copy	1	
64	LEMLAND – Source Language Focal	Focal 8-81	no date	Copy	1	

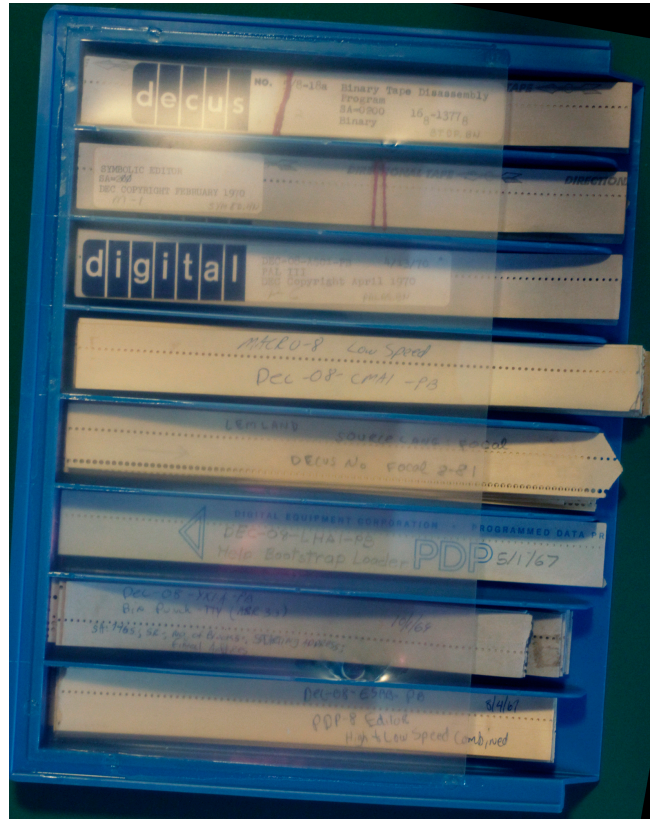
65	Focal 5/69 Binary (1)Focal Interpreter & Arithmetic Package (2) Extended functions (3) 2-User Patch FOCL.S 4K	FOCAL 8-52a	7/25/72	DECUS Original	1
66	Logical Shift Subroutines	FOCAL 8-148A	no date	DECUS	1
67	Logical Shift Subroutines	DIGITAL-8-9-U-ASCII	3/20/65	DEC Original	1
68	Logical Shift Subroutines	DIGITAL-8-9-U-ASCII	3/20/65	DEC Original	1
69	Arithmetic Shift Subroutines	DIGITAL-8-8-U-ASCII	3/15/65	DEC Original	1
70	Macro-8 Low Speed	DIGITAL-8-8-S-BIN	2/3/66	DEC Original	1
71	Logical Subroutines	DIGITAL-8-7-U-ASCII	3/20/65	DEC Original	1
72	Octal Memory Dump	DIGITAL-8-6-U-ASCII	3/17/65	DEC Original	1
73	Fortran Symbol Print use w/8-2-S Compiler	DIGITAL-8-6-S	8/8/65	DEC Original	1
74	Binary Punch Routine (33ASR)	DIGITAL-8-5-U-Bin	3/1/65	DEC Original	2
75	Binary Punch Routine (33ASR) SA:7465	DIGITAL-8-5-U-Bin	3/1/65	DEC Original	1
76	Floating Point Package 3 Interpreter;I/O;Functions	DIGITAL-8-5-S-BIN	4/18/65	DEC Original	1
77	Floating Point Package 4 Interpreter;I/O;I/O Controller	DIGITAL-8-5-S-BIN	4/18/65	DEC Original	1
78	Floating Point Package 1 Basic System	DIGITAL-8-5-F-BIN	4/18/65	DEC Original	1
79	Floating Point Package 2 Interpreter;I/O;I/O Controller	DIGITAL-8-5-F-BIN	4/18/65	DEC Original	1
80	RIM Punch 33	DIGITAL-8-4-U-BIN	3/1/65	DEC Original	1
81	DDT	DIGITAL-8-4-S-BIN	3/9/65	DEC Original	1
82	PAL III Extended Symbols	DIGITAL-8-3-S-BIN	4/19/66	DEC Original	1
83	PAL III SA:200	DIGITAL-8-3-S-BIN	4/19/66	DEC Original	1
84	Double Precision Decimal to Binary Conversion	DIGITAL-8-29-U-ASCII	1/14/66	DEC Original	1
85	Signed Decimal Print, Double Precision	DIGITAL-8-25-U-ASCII	1/19/66	DEC Original	1
86	Floating Point Package 3 Interpreter;I/O;Functions (EAE)	DIGITAL-8-25-S-BIN	12/12/65	DEC Original	1
87	Floating Point Package 4 Interpreter;I/O;I/O Controller (EAE)	DIGITAL-8-25-S-BIN	12/12/65	DEC Original	1
88	Floating Point Package 2 Interpreter;I/O;I/O Controller (EAE)	DIGITAL-8-25-F-BIN	12/12/65	DEC Original	1
89	Unsigned Decimal Print, Double Precision	DIGITAL-8-24-U-ASCII	1/19/66	DEC Original	1
90	Signed Decimal Print, Single Precision	DIGITAL-8-23-U-ASCII	1/19/66	DEC Original	1
91	2's Compliment Double Precision Multiply (EAE Type 182 Version)	DIGITAL-8-23-F-ASCII	no date	DEC Original	1
92	Unsigned Decimal Print Subroutine	DIGITAL-8-22-U-ASCII	6/7/65	DEC Original	1
93	Signed Divide (Used EAE Type 182) Single Precision	DIGITAL-8-22-F-ASCII	6/24/65	DEC Original	1
94	Symbolic Tape Generator	DIGITAL-8-21-U-BIN	5/2/65	DEC Original	1
95	Signed Multiply (Uses EAE Type 182) Single Precision	DIGITAL-8-21-F-ASCII	6/3/65	DEC Original	1
96	Character String Typeout	DIGITAL-8-20-U-ASCII	10/22/65	DEC Original	1
97	4 Word Floating Point Package	DIGITAL-8-20-F-BIN	5/30/66	DEC Original	1
98	Binary Loader (ASR 33, 750, 183 Memory Extension)	DIGITAL-8-2-U-BIN	3/23/66	DEC Original	1
99	Fortran Compiler Revised 12/14/65	DIGITAL-8-2-S-BIN	12/14/65	DEC Original	1
100	Fortran Operating System	DIGITAL-8-2-S	8/13/65	DEC Original	1
101	Teletype Output Package	DIGITAL-8-19-U-ASCII	4/7/65	DEC Original	1
102	Message Rev.	DIGITAL-8-18-U-ASCII	10/22/65	DEC Original	1
103	Double Precision Cosine	DIGITAL-8-18-F-ASCII	7/10/65	DEC Original	1
104	EAE (Type 182) Instruction Set Simulator	DIGITAL-8-17-U-ASCII	7/9/65	DEC Original	1
105	Master Tape Duplicator	DIGITAL-8-16-S-BIN	7/10/65	DEC Original	1
106	Double Precision Sine	DIGITAL-8-16-F-ASCII	7/7/65	DEC Original	1
107	Binary to Binary Coded Decimal Conversion (Four Digit)	DIGITAL-8-15-U-ASCII	6/7/65	DEC Original	1
108	Binary to Binary Coded Decimal Conversion	DIGITAL-8-14-U-ASCII	3/10/65	DEC Original	1
109	Double Precision Divide	DIGITAL-8-14-F-ASCII	6/21/65	DEC Original	1
110	2's Compliment Double Precision Multiply	DIGITAL-8-13-F-ASCII	6/5/65	DEC Original	1
111	Incremental Plotter Subroutine	DIGITAL-8-12-U-ASCII	6/1/66	DEC Original	1
112	ODT-II SA:1000 (Low)	DIGITAL-8-12-S-BIN	8/26/65	DEC Original	1
113	ODT-II SA:7000 (High)	DIGITAL-8-12-S-BIN	8/26/65	DEC Original	1
114	2's Compliment Divide Subroutine	DIGITAL-8-12-F-ASCII	7/65	DEC Original	1
115	Double Precision BCD to Binary Conversion	DIGITAL-8-11-U-ASCII	3/17/66	DEC Original	1
116	Signed Multiply Subroutine Single Precision	DIGITAL-8-11-F-ASCII	7/28/66	DEC Original	1
117	BCD to Binary Conversion	DIGITAL-8-10-U-ASCII	3/3/66	DEC Original	1
118	Calculator (2 Parts with Floating Point Package) SA=200	DIGITAL-8-10-S-BIN	6/28/65	DEC Original	1
119	RIM Loader SA=7756	DIGITAL-8-1-U-RIM	2/23/65	DEC Original	1
120	Symbolic Tape Editor SA 176, 177, 200	DIGITAL-8-1-S-BIN	8/4/67	DEC Original	1
121	Micro-8 An on-line Assembler SA=3200	DECUS 8-91		DECUS	1
122	Tiny Tape Editor Binary	DECUS 8-52		DECUS	1
123	Tiny Tape Editor Listing	DECUS 8-52		DECUS	1
124	Tiny Tape Editor Symbolic 1	DECUS 8-52		DECUS	1
125	Tiny Tape Editor Symbolic 2	DECUS 8-52		DECUS	1
126	Blackjack SA=200	DECUS 8-44A		Copy	1
127	Calculator With Modified Fixed Point Output Binary SA:200	DECUS 8-44		DECUS Original	1
128	Floating Output Controller ASCII Binary	DECUS 8-44		DECUS	1
129	Floating Output Controller ASCII Symbolic	DECUS 8-44		DECUS	1
130	Calendar SA=200	DECUS 8-388	no date	DECUS	1
131	Binary & RIM Consolidator BRMC 9 Assembly 3 rd Pass c COM.	DECUS 8-374		DECUS Original	1
132	CBL RIM	DECUS 8-26A		Copy	1
133	4K ALGOL Algol Function Loader & Library #1	DECUS 8-213	no date	DECUS	1
134	4K ALGOL Object Time System #3	DECUS 8-213	no date	DECUS	1
135	4K ALGOL ALGOL Compiler Reinitialization #4	DECUS 8-213	no date	DECUS	1
136	4K ALGOL ALGOL Compiler Identifier Printout #2	DECUS 8-213	no date	DECUS	1
137	4K ALGOL ALGOL Loader Overlay #5	DECUS 8-213	no date	DECUS	1
138	4K ALGOL ALGOL Compiler #6	DECUS 8-213	no date	DECUS	1
139	4K ALGOL PDP-8 ALGOL Object Time System Function Loader & Library #7	DECUS 8-213	no date	DECUS Original	1
140	Copy SA=0200 Binary	DECUS 8-177		DECUS	1
141	Cinet BASIC SA:200 Binary w/corrections added	DECUS 8-159	3/7/73	DECUS	1
142	Cinet BASIC SA:200 Binary for use with PDP-8/S only	DECUS 8-159	no date	DECUS	1
143	Character Packing and Unpacking Routines Binary	DECUS 5/8-51	no date	DECUS	1

labeled bad tape 10-

144	Character Packing and Unpacking Routines Binary	DECUS 5/8-51		DECUS	1	
145	Character Packing and Unpacking Routines Listing	DECUS 5/8-51		DECUS	1	
146	Character Packing and Unpacking Routines Symbolic	DECUS 5/8-51		DECUS	1	
147	Utility Programs Use with High Speed Paper Tape Reader & ASR-33 Binary	DECUS 5/8-46a	no date	DECUS Original	1	
148	Phoenix Assembler Pal III Modifications Binary	DECUS 5/8-28a		DECUS	1	
149	Triple Precision Package Binary	DECUS 5/8-21	10/5/65	DECUS	1	
150	Triple Precision Package Binary	DECUS 5/8-21	10/5/65	DECUS	1	
151	Binary Taper Disassembly Program SA:200 Binary	DECUS 5/8-18a	no date	DECUS	1	
152	Paper Tape Duplicator Binary	DECUS 5-16		Copy	1	Probably DECUS
153	Disk System Builder SA=0200	DEC-D8-SBAD-PB	1/7/68	DEC Original	1	
154	Disk System Builder SA=0200	DEC-D8-SBAD-PB	11/7/68	DEC Original	1	
155	Disk System Builder	DEC-D8-SBAC-PB	11/1/67	DEC Original	1	
156	Disk System Builder SA=0200	DEC-D8-SBAC-PB	4/17/68	DEC Original	1	
157	Disk System Builder	DEC-D8-SBAB-PB	11/1/67	DEC Original	1	
158	Disk System PIP (DF32) Save PIP! 0-5177;1000	DEC-D8-PDAC-PB	8/6/69	DEC Original	1	
159	Disk PIP Save PIP! 0-5177;1000	DEC-D8-PDAB-PB	9/18/68	DEC Original	1	
160	Disk PIP Save PIP! 0-5177;1000	DEC-D8-PDAB-PB	9/18/68	DEC Original	1	
161	Disk PIP Save PIP! 0-5177;1000	DEC-D8-PDAB-PB	9/18/68	DEC Original	1	
162	Disk Editor Save Edit: 0-3177	DEC-D8-ESAB-PB	4/24/68	DEC Original	1	
163	Disk Editor Save Edit: 0-3177	DEC-D8-ESAB-PB	4/24/68	DEC Original	1	
164	Disk System DDT Save .DDT	DEC-D8-CDD2-PB	12/27/67	DEC Original	1	
165	Disk System DDT Save .DDT	DEC-D8-CDD2-PB	12/27/67	DEC Original	1	
166	Disk System DDT Save .DDT	DEC-D8-CDD2-PB	12/27/67	DEC Original	1	
167	Disk System DDT Driver Save DDT:7200-7577	DEC-D8-CDD1-PB	12/27/67	DEC Original	1	
168	Disk System DDT Driver Save DDT:7200-7577	DEC-D8-CDD1-PB	12/27/67	DEC Original	1	
169	Disk System DDT Driver Save DDT:7200-7577	DEC-D8-CDD1-PB	12/27/67	DEC Original	1	
170	DR1025 ASCII	DEC-D8-CDD1-PA	12/15/67	DEC Original	1	
171	DR10 25 ASCII	DEC-D8-CDD1-PA	12/15/67	DEC Original	1	misprint?
172	PAL D Disc Assembler	DEC-D8-ASAB-PB	8/14/69	DEC Original	1	
173	PAL D Disc Assembler (PD403A) Save PalD: 0-7577;6200	DEC-D8-ASAA-PB	4/25/68	DEC Original	1	
174	PAL D Disc Assembler (PD403A) Save PalD: 0-7577;6200	DEC-D8-ASAA-PB	4/25/68	DEC Original	1	
175	PAL D Disc Assembler (PD403A) Save PalD: 0-7577;6200	DEC-D8-ASAA-PB	4/25/68	DEC Original	1	
176	Fortran D Diagnose use w/Fortran OS	DEC-D8-AFA6-PB	3/20/68	DEC Original	1	
177	Fortran D Symbol Print (STBL) use w/Fortran D Compiler	DEC-D8-AFA5-PB	3/20/68	DEC Original	1	
178	Fortran D Symbol Print (STBL) use w/Fortran D Compiler	DEC-D8-AFA5-PB	3/20/68	DEC Original	1	
179	Fortran D Symbol Print (STBL) use w/Fortran D Compiler	DEC-D8-AFA5-PB	3/20/68	DEC Original	1	
180	Fortran D Operating System Save .OS.	DEC-D8-AFA4-PB	3/23/68	DEC Original	1	
181	Fortran D Operating System Save .OS.	DEC-D8-AFA4-PB	3/23/68	DEC Original	1	
182	Fortran D Operating System Loader Save Fosl	DEC-D8-AFA3-PB	3/23/68	DEC Original	1	
183	Fortran D Operating System Loader Save Fosl	DEC-D8-AFA3-PB	3/23/68	DEC Original	1	
184	Fortran D Operating System Loader Save Fosl	DEC-D8-AFA3-PB	3/23/68	DEC Original	1	
185	Fortran D Compiler Save .FT.	DEC-D8-AFA2-PB	3/21/68	DEC Original	1	
186	Fortran D Compiler Save .FT.	DEC-D8-AFA2-PB	3/21/68	DEC Original	1	
187	Fortran D Compiler Save .FT.	DEC-D8-AFA2-PB	3/21/68	DEC Original	1	
188	Fortran D Compiler Loader Save Fort!01777;200	DEC-D8-AFA1-PB	3/21/68	DEC Original	1	
189	Fortran D Compiler Loader Save Fort!01777;200	DEC-D8-AFA1-PB	3/21/68	DEC Original	1	
190	Fortran D Compiler Loader Save Fort!01777;200	DEC-D8-AFA1-PB	3/21/68	DEC Original	1	
191	ODT (Low)	DEC-C0C1-PB	11/10/67	DEC Original	1	
192	Bin Punch - TTY SA:7465 (ASR-33)	DEC-08-YXIA-PB	10/1/69	Copy	1	
193	Binary Punch Routine (33ASR)	DEC-08-YX1A-PB	10/1/69	Copy	1	
194	Floating Point Package 4 Interpreter;I/O;I/O Controller	DEC-08-YQ4A-PB	4/18/65	DEC Original	1	
195	Floating Point Package 3 Interpreter;I/O;Functions	DEC-08-YQ3A-PB	4/18/65	DEC Original	1	
196	Floating Point Package 2 Interpreter;I/O;I/O Controller	DEC-08-YQ2A-PB	4/18/65	DEC Original	1	
197	Floating Point Package 1 Basic System	DEC-08-YQ1A-PB	4/18/65	DEC Original	1	
198	DTC-8 DecTape Copy Routine for Copying 8, 9, 10 Formats	DEC-08-YPTA-PB	11/12/68	Copy	1	
199	Octal Memory Dump	DEC-08-YPPA-PB	10/10/69	Copy	1	
200	TC01-TU55 DecTape Formatter	DEC-08-UDTFA-A-PB	8/4/77	DEC Original	1	
201	High Memory RIM Punch 33	DEC-08-PMP2-PB	11/22/66	DEC Original	1	
202	High Memory RIM Punch 33	DEC-08-PMP2-PB	11/22/66	Copy	1	
203	High Memory RIM Punch 33	DEC-08-PMP2-PB	11/22/66	DEC Original	1	
204	High Memory RIM Punch 33	DEC-08-PMP2-PB	11/22/66	Copy	1	
205	Low Memory RIM Punch 33	DEC-08-PMP1-PB	11/22/66	DEC Original	1	
206	Low Memory RIM Punch 33	DEC-08-PMP1-PB	11/22/66	Copy	1	
207	Read In Mode Punch (RIM) Punch Low Memory-33	DEC-08-PMP1-PB	11/22/66	DEC Original	1	
208	TC01 Bootstrap Loader	DEC-08-LUAA-PB	2/17/67	Copy	1	
209	Help Generator	DEC-08-LHA2-PB	5/1/67	Copy	1	
210	Help Bootstrap Loader	DEC-08-LHA1-PB	5/1/67	Copy	1	
211	Basic 8 for 8K PDP-8 SA=1000	DEC-08-LBASA-PB	2/10/72	DEC Original	1	
212	Binary Loader	DEC-08-LBAA-PM	5/10/67	Copy	1	Questionable
213	Binary Loader	DEC-08-LBAA-PM	5/10/67	DEC Original	1	
214	Binary Loader	DEC-08-LBAA-PM	5/10/67	DEC Original	1	
215	Binary Loader	DEC-08-LBAA-PM	5/10/67	DEC Original	1	
216	Binary Loader PDP-8	DEC-08-LBAA-PB	5/10/67	Copy	1	
217	TC01 DecTape Subroutines ASCII Tape	DEC-08-FUB0-PA	1/8/68	Copy	1	
218	Symbolic Editor SA=200	DEC-08-ESAC-PB	2/4/70	Copy	1	
219	PDP-8 Editor High + Low Speed Combined	DEC-08-ESAB-PB	8/4/67	DEC Original	1	
220	PDP-8 Editor High + Low Speed Combined	DEC-08-ESAB-PB	8/4/67	DEC Original	1	

221	PDP-8 Editor High + Low Speed Combined	DEC-08-ESAB-PB	8/4/67	Copy	1
222	Disk Editor Save Edit: 0-3177	DEC-08-ESAB-PB	4/24/68	DEC Original	1
223	EDU-10 4K BASIC	DEC-08-ED10A-A-PB	8/30/72	DEC Original	1
224	ODT (High)	DEC-08-COC2-PB	11/10/67	DEC Original	1
225	ODT (Low)	DEC-08-COC1-PB	11/10/67	DEC Original	1
226	Macro 5/8 High Speed I/O	DEC-08-CMA2-PB	2/3/66	DEC Original	1
227	Macro-8 Low Speed	DEC-08-CMA1-PB	2/3/66	DEC Original	1
228	Macro-8 Low Speed	DEC-08-CMA1-PB	no date	Copy	1
229	DDT-8	DEC-08-CDDB-PB	8/14/69	DEC Original	1
230	DDT-8	DEC-08-CDDB-PB	8/14/69	Copy	1
231	DDT-8	DEC-08-CDDB-PB	8/14/69	DEC Original	1
232	DDT SA= 5400	DEC-08-CDDA-PB	4/15/67	DEC Original	1
233	ODT (High)	DEC-08-C0C2-PB	11/10/67	DEC Original	1
234	PAL III Extended Symbols	DEC-08-ASC2-PA	4/27/70	DEC Original	1
235	PAL III	DEC-08-ASC1-PB	4/13/70	DEC Original	1
236	PAL III	DEC-08-ASC1-PB	4/13/70	DEC Original	1
237	PAL III Extended Symbols	DEC-08-ASB2-PB-ASCII	2/24/67	DEC Original	1
238	PAL III	DEC-08-ASB1-PB	2/24/67	DEC Original	1
239	PAL III Binary	DEC-08-ASB1-PB	2/24/67	DEC Original	1
240	Focal 1969 + Init	DEC-08-AJAE-PB	7/9/69	Copy	1
241	Focal.W SA:0200	DEC-08-AJAD-PB	8/4/68	DEC Original	1
242	Focal '68	DEC-08-AJAB-PB	4/29/68	DEC Original	1
243	Focal SA:200	DEC-08-AJAB-PB	4/29/68	DEC Original	1
244	Multi-user Overlays for Focal 1969 (Libra.DF32, DISKIN.DF32)	DEC-08-AJ5E-PB	7/9/69	DEC Original	1
245	Dual User 8K Overlay for Focal.W	DEC-08-AJ2D-PB	8/4/68	DEC Original	1
246	Utility Overlays for Focal 1969 (4 word, 8K)	DEC-08-AJ1E-PB	7/9/69	DEC Original	2
247	Utility Overlays for Focal 1969 (4 word, 8K)	DEC-08-AJ1E-PB	7/9/69	Copy	1
248	4K Fortran Operating System	DEC-08-AFC3-PB	8/1/67	DEC Original	1
249	Fortran Operating System	DEC-08-AFC3-PB	8/1/67	DEC Original	1
250	Fortran Operating System	DEC-08-AFC3-PB	8/67	DEC Original	1
251	4K Fortran Compiler	DEC-08-AFC1-PB	9/13/67	DEC Original	1
252	Fortran Compiler	DEC-08-AFC1-PB	9/13/67	DEC Original	1
253	Fortran Compiler	DEC-08-AFC1-PB	9/13/67	DEC Original	1
254	Fortran D Operating System Save .OS.	DEC-08-AFA4-PB	3/23/68	DEC Original	1
255	Fortran Operating System Reassembled 3/2/67 From 10/11/66 In PDP-8 Language	DEC-08-AFA3-PB	3/2/67	DEC Original	1
256	Fortran Symbol Print use w/08-AFC1-PB compiler	DEC-08-AFA2-PB	8/8/65	DEC Original	2
257	Fortran Symbol Print use w/08-AFC1-PB compiler	DEC-08-AFA2-PB	8/8/65	DEC Original	1
258	8K SABR Assembler (V16)	DEC-08-A2D2-PB	10/30/69	DEC Original	1
259	8K-32 Linking Loader	DEC-08-A2C7	10/30/69	DEC Original	1
260	8 To 32K Linking Loader (labeled Fails Parity)	DEC-08-A2C3-PB	10/30/69	DEC Original	1
261	8K Fortran Library Dectape I/O	DEC-08-A2B6-PR	4/28/69	DEC Original	1
262	8K Fortran Library Subroutines tape 2 of 2	DEC-08-A2B5-PR	2/28/69	DEC Original	1
263	8K Fortran Library Subroutines tape 1 of 2	DEC-08-A2B4-PR	2/28/69	DEC Original	1
264	8 To 32K Linking Loader SA:200	DEC-08-A2B3-PB	2/2/69	DEC Original	1
265	8K Fortran Compiler SA:1000	DEC-08-A2B1-PB	2/28/69	DEC Original	1
266	Symbolic Editor SA=200		2/11/70	Appears DEC	1
267	DMOV		11/8/70	Copy	1
268	TDISK GGC		11/14/70	Copy	1
269	Floppy Disk Resident Monitor (DF32) Overlay		7/1/74	Copy	1
270	Script (prints banners in script on teletype)		4/20/76	Copy	1
271	Bin Loader Special (?)			Copy	1
272	Binary Loader			Copy	1
273	Binary Loader			Copy	1
274	Binary Punch Routine SA=7465			Copy	1
275	Cold Start Binary Load w/Bin Loader LA:6200			Copy	1
276	Disassembler Patch for EAE			Copy	1
277	Disassembler Patch to allow 6 Letter Symbols			Copy	1
278	Disassembler			Copy	1
279	Disk Focal, Disk Fnew, Field 1 Fnew			Copy	1
280	Disk System Builder SA=0200			Copy	1
281	Disk To/From Tape			Copy	1
282	Edit			Copy	1
283	EDU-20 BASIC, 5 User			Copy	1
284	Focal '69 4K Init			Copy	1
285	Focal 69 Patch for Computed Line Number			Copy	1
286	Focal 8-80 HSP Focal Patch			Copy	1
287	Focal-2			Copy	1
288	One tray of miscellaneous paper tapes probably games and utilities dated late '60s to early '70s			Teletype copies	1
289	Patch for Cross Reference			Copy	1
290	RF08 Disk Restore			Copy	1
291	Sample Program Fortran Sym Desk Calculator			DEC Original	1
282	SMSRCH			Copy	1

The following pictures are sample of the papertape software included in the auction:



Documentation

Documentation of the hardware and software includes manuals, engineering drawings and discrete documents describing particular hardware and software components. Please be advised that there may be documentation for hardware or software components not included in this auction.

PDP-8 Software Documentation

	Product Name	Part Code	Date	Type	Author	Copies	Previous Code
1	PDP-8 Instruction Test Part 1	MAINDEC-08-801-1	5/14/63	Copy		1	
2	PDP-8 Instruction Test Part - 2A	MAINDEC-08-801-2A	2/2/66	Copy		1	
3	PDP-8 Instruction Test Part (EAE Type)	MAINDEC-08-801-3A	7/19/65	Copy		1	
4	PDP-8 Instruction Test Part 2B	MAINDEC-08-D02B-D	1/12/68	Original		1	
5	PDP-8 Instruction Test Part 2B	MAINDEC-08-D02B-D	1/12/68	Copy	Diagnostic Group	1	
6	Random JMP-JMS Test	MAINDEC-08-D05B-D	12/28/67	Original	R. Green	2	
7	PDP-8 Instruction Test EAE	MAINDEC-08-D0BA-D	7/22/65	Copy	M. Horowitz	1	MAINDEC 801-3B
8	PDP-8 Instruction Test EAE	MAINDEC-08-D0BA-D	7/22/65	Original	M. Horowitz	1	MAINDEC 801-3B
9	PDP-8 Memory Power On/Off Test	MAINDEC-08-D1AC-D	9/16/68	Copy	M. Horowitz	1	MAINDEC 829
10	Memory Address Test	MAINDEC-08-D1B0-D	1/12/68	Original	R. Green	1	MAINDEC-08-D11A-D
11	Memory Address Test	MAINDEC-08-D1B0-D	3/25/68	Original	R. Green	1	MAINDEC-08-D11A-D
12	Basic PDP-8, 8/I Extended Memory	MAINDEC-08-D1EB-D	5/1/68	Copy	J. W. Richardson	1	
13	PDP-8, 8/I Extended Memory	MAINDEC-08-D1EB-D	5/1/68	Original	J. W. Richardson	1	
14	PDP-8, 8/I Extended Memory	MAINDEC-08-D1EC-D	11/1/71	Original	J. Richardson - J. Vrobel	1	
15	PDP-8, 8/I, 8/S Extended Memory Control	MAINDEC-08-D1GB-D	5/5/68	Copy	J. Richardson	1	
16	PDP-8, 8I, 8S Extended Memory Control	MAINDEC-08-D1GB-D	5/5/68	Original	J. Richardson	1	
17	PDP-8, 8I, 8S Extended Memory Control	MAINDEC-08-D1GD-D	7/27/70	Original	J. Richardson /L.	1	
18	PDP-8, 8/I, Extended Memory Address	MAINDEC-08-D1HA-D	3/13/68	Copy	J. W. Richardson	1	
19	PDP-8, 8/I, Extended Memory Address	MAINDEC-08-D1HA-D	3/13/68	Original	J. W. Richardson	1	
20	Basic PDP-8, 8/I Memory Checkerboard (Labeled Advance Copy)	MAINDEC-08-D1J0-D (L)	2/1/68	Original	J. W. Richardson	1	
21	Basic PDP-8, 8/I Memory Checkerboard	MAINDEC-08-D1L0	6/10/68	Copy	J. W. Richardson	1	
22	Basic PDP-8, 8/I Memory Checkerboard	MAINDEC-08-D1L0	6/10/68	Original	J. W. Richardson	1	
23	Family-of-8 ASR 33/35 Teletype Tests	MAINDEC-08-D2PE-D	2/21/69	Copy	Diagnostic Group	1	
24	Family-of-8 ASR 33/35 Teletype Tests	MAINDEC-08-D2QD-D	6/4/68	Copy	Diagnostic Group	1	
25	TC01 Basic Exerciser	MAINDEC-08-D3BC-D	10/13/69	Copy	Diagnostic Group	1	
26	TC01 Extended Memory Exerciser	MAINDEC-08-D3EB-D	1/5/68	Copy	Edward P. Steinberger	1	
27	DECTREX 1 TC01 Random Exerciser	MAINDEC-08-D3RA-D	1/9/67	Copy	Keith F. Nelson	1	MAINDEC 851
28	DF32 Discless Logic Test, Minidisc	MAINDEC-08-D5BB-D	11/3/67	Copy	J. Hittrell	1	

29	DF32 Disk Data mini Disk, Interface, Address, Data Test	MAINDEC-08-D5CC-D	4/4/68	Copy	John L. Hittrell	1
30	DF32 Disk Data mini Disk, Interface, Address, Data Test	MAINDEC-08-D5CC-D	4/4/65	Original	John L. Hittrell	1
31	DF32 Multi Disk	MAINDEC-08-D5DB-D	8/22/68	Copy	E. Haight	1
32	DF32 Multi Disk	MAINDEC-08-D5DB-D	8/22/68	Original	E. Haight	1
33	PDP-8, 8I, 8S Extended Memory Control	MAINDEC-08-DGMCA-A-D	3/29/73	Original	John Vrobel	1
34	Random ISZ Test	MAINDEC-08-DO7B-D	3/25/68	Original	R. Green	2
35	Random ISZ Test	MAINDEC-08-DO7B-D	3/25/68	Copy	R. Green	2
36	Float 1's & 0's Through Memory	MAINDEC-12-D1EA-D	9/23/69	Original	James Kelly	1
37	Instruction Test 1	MAINDEC-8/I-D01B-D	3/25/68	Copy	M. Horowitz	1
38	Instruction Test 2	MAINDEC-8/I-D02B-D	3/21/68	Copy	M. Horowitz	1
39	MM8-E 4K Memory Checkerboard	MAINDEC-8E-D1AA-D (D)	8/3/70	Original	Vernon Frey	1
40	MM8-E 4K Memory Checkerboard (Labeled Advance Copy)	MAINDEC-8E-D1AA-D (D)	8/3/70	Original	Vernon Frey	1
41	KM8-E Extended Mem Checkerboard (Labeled Advance Copy)	MAINDEC-8E-D1BB-D (D)	11/24/70	Original	Vernon Frey	1
42	Memory Address Test (Labeled Advance Copy)	MAINDEC-8E-D1EB-D	12/10/70	Original	Bruce Hansen	1
43	PDP-8E Extended Mem Addr Test (Labeled Advance Copy)	MAINDEC-8E-D1FA-D-(D)	11/25/70	Original	Vernon Frey	1
44	PDP-8E Memory Power On/Off Test (Labeled Advance Copy)	MAINDEC-8E-D1GA-D-(D)	10/28/70	Original	Bruce Hansen	1
45	PDP-8/E Memory Extension and Time Share Control Test (Labeled Advance Copy)	MAINDEC-8E-D1HA-D-(D)	11/1/70	Original	J. Vrobel	1
46	PDP-8 Instruction Test Part 1	MAINDEC 801-1	5/14/63	Original		1
47	PDP-8 Instruction Test Part -- 2A	MAINDEC 801-2A	2/2/66	Original		1
48	PDP-8 Instruction Test Part -- 2A Change Notice	MAINDEC 801-2A	5/1/66	Original		1
49	Random JMP-JMS Test (missing page 1)	MAINDEC 801-2C	Unknown	Original		1
50	PDP-8 Instruction Test Part (EAE Type 182)- 3A	MAINDEC 801-33A	7/19/65	Original		1
51	Memory Checkerboard Test	MAINDEC 802	9/1/65	Copy		1
52	Memory Checkerboard Test	MAINDEC 802	9/1/65	Original		1
53	PDP-8 Memory Address Test	MAINDEC 803	5/11/65	Copy		1
54	PDP-8 Memory Address Test	MAINDEC 803	5/11/65	Original		1
55	PDP-8 Teletype Reader Test	MAINDEC 810	4/12/65	Copy		1
56	PDP-8 Teleprinter Test	MAINDEC 810	4/12/65	Original		1
57	PDP-8 Teletype Punch Test	MAINDEC 812	5/5/65	Copy		1
58	PDP-8 Teletype Punch Test	MAINDEC 812	5/5/65	Original		1
59	PDP-8 Teleprinter Test	MAINDEC 814	4/29/65	Copy		1
60	PDP-8 Teleprinter Test	MAINDEC 814	4/29/65	Original		1
61	PDP-8 LT08 Teleprinter Test	MAINDEC 828	12/1/65	Original		1
62	PDP-8 Memory Power On/Off Test	MAINDEC 829	12/2/65	Original		1
63	ALGOL-8	DIGITAL-08-KAYA-D	3/31/69	Original	J. C. Broussard et al.	1
64	Read-In-Mode Loader	DIGITAL-8-1-U	2/23/65	Original		1
65	Binary Coded Decimal to Binary Conversion Subroutine	DIGITAL-8-10-U-Sym	9/1/65	Original		1
66	Double Precision Binary Coded Decimal to Binary	DIGITAL-8-11-U	3/18/65	Original		1
67	Signed Double Precision Multiply	DIGITAL-8-13-F	6/5/65	Original		1
68	Double-Precision Signed Divide Subroutine	DIGITAL-8-14-F	6/21/65	Original		1
69	Binary-to-Binary-Coded-Decimal	DIGITAL-8-14-U-Sym	3/10/65	Original		1
70	Binary-to-Binary-Coded-Decimal Conversion (Four Digit)	DIGITAL-8-15-U-Sym	6/7/65	Original		1
71	Extened Arithmetic Element Type 182, Instruction Set Simulator	DIGITAL-8-17-U	7/9/65	Original		1
72	Arithmetic Message Typeout	DIGITAL-8-18-U-Sym	10/22/65	Original		1
73	Teletype Output Subroutine	DIGITAL-8-19-U-Sym	4/7/65	Original		1
74	Four Word Floating Point Package	DIGITAL-8-20-F-Bin	6/26/65	Original		1
75	Character String Typeout	DIGITAL-8-20-U-Sym	10/22/65	Original		1
76	Signed Single-Precision Multiply Subroutine Using the Extended Arithmetic Element Type 182	DIGITAL-8-21-F	6/30/65	Original		2
77	Symbolic Tape Format Generator	DIGITAL-8-21-U-Sym	5/2/65	Original		1
78	Signed Single-Precision Divide Subroutine Using the Extended Arithmetic Element Type 182	DIGITAL-8-22-F	6/24/65	Original		2
79	Unsigned Decimal Print	DIGITAL-8-22-U-Sym	6/7/65	Original		1
80	Signed Double-Precision Multiply Subroutine Using the Extended Arithmetic Element Type 182	DIGITAL-8-23-F	6/30/65	Original		2
81	Unsigned Decimal Print, Double Precision	DIGITAL-8-24-U-Sym	1/19/66	Original		1
82	Double Precision Decimal-to-Binary Conversion and Input (ASR-33) (Signed or Unsigned)	DIGITAL-8-29-U-Sym	1/14/66	Original		1
83	DEctape Library System Loader	DIGITAL-8-3-U	3/17/65	Original		1
84	Read-In-Mode (RIM) Punch 33	DIGITAL-8-4-U-Rim	3/1/65	Original		1
85	Octal Memory Dump	DIGITAL-8-6-U-Sym	3/17/65	Original		1
86	Logical Subroutines	DIGITAL-8-7-U-Sym	3/20/65	Original		1
87	Arithmetic Shift Subroutines	DIGITAL-8-8-U-Sym	3/15/65	Original		1
88	Logical Shift Subroutines	DIGITAL-8-9-U-Sym	3/20/65	Original		1
89	PDP-8 Dual Process System	DECUS 8/8S-77	May-67	Original	Richard M. Merrill	1
90	BCD to Binary Conversion of 3-Digit	DECUS 5-6	9/18/64	Original	Donald V. Weaver	1
91	Triple Precision Arithmetic Package	DECUS 5/8-21	10/5/65	Original	Joseph A. Rodnite	1

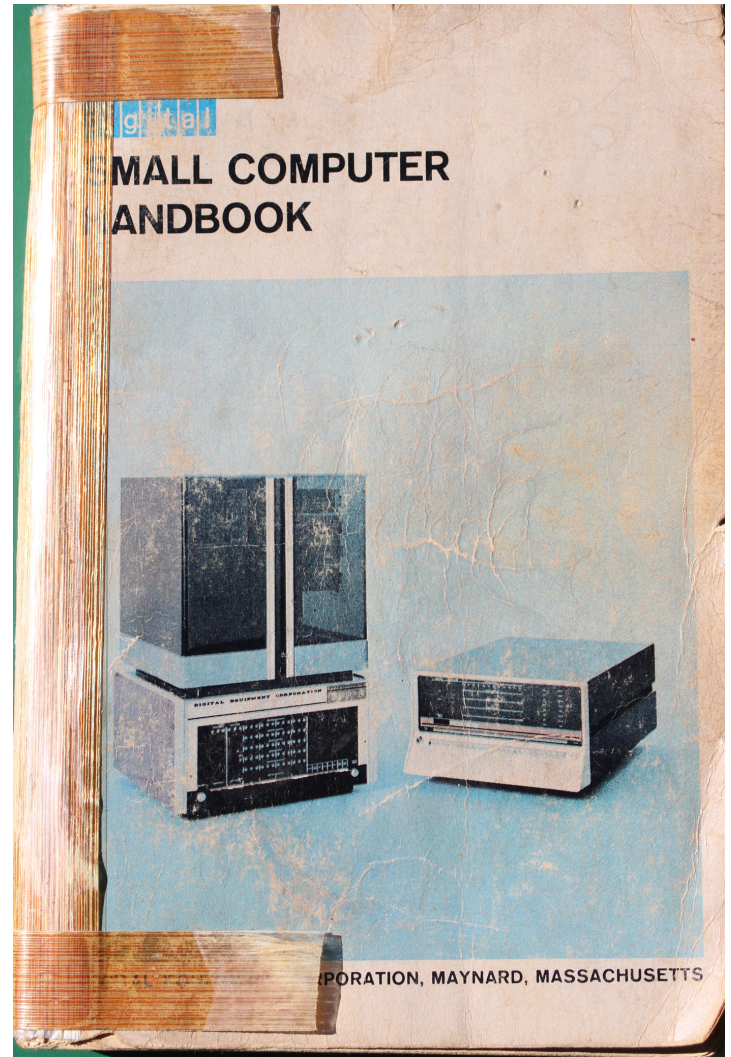
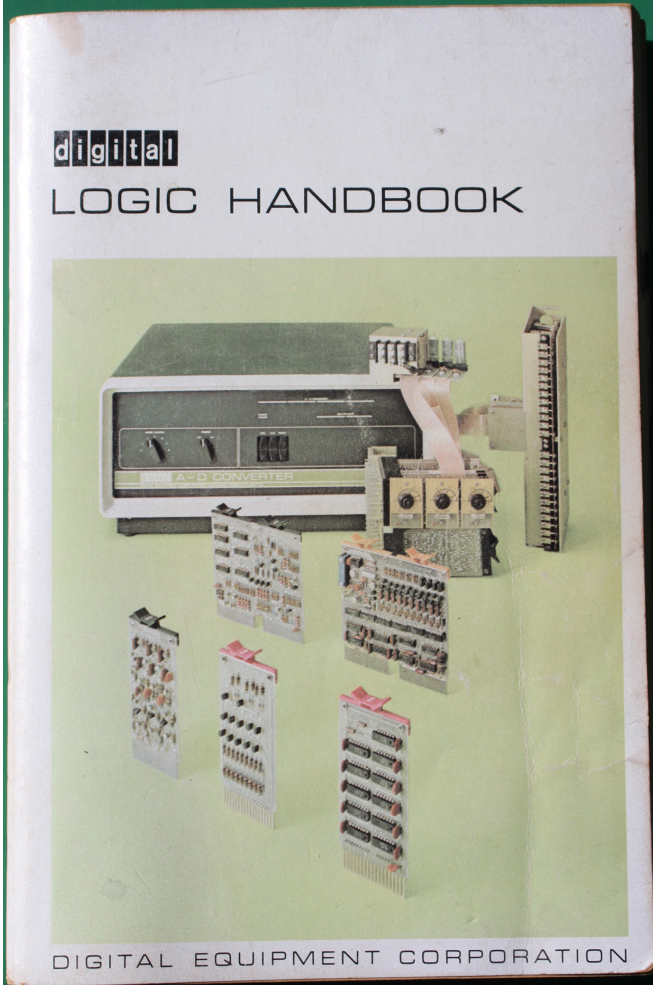
92	Ftype – Fractional Type	DECUS 5/8-38	7/22/65	Original	P. T. Brady	1
93	Character Packing and Unpacking Routine	DECUS 5/8-51	Oct-66	Original	Richard M. Merrill	1
94	Decimal to Binary Conversion by Radix Deflation and Accelerated Radix Deflation	DECUS 5/8-7	12/64 & 1/65	Original	Donald V. Weaver	1
95	ATEPO (Auto Test in Elementary Programming and Operation of a PDP-5 Computer)	DECUS 5/8/-15	Oct-67	Original	Rutgers University EE Dept.	1
96	Double Precision BCD Arithmetic Package (Incomplete)	DECUS 8-100	Jul-67	Original	Richard M. Merrill	1
97	High Speed Executive for the PDP-8, PDP-98	DECUS 8-146	7/5/68	Original	R. L. Steel	1
98	PDP-8 Music Programs	DECUS 8-152a	Jul-69	Original	R. G. Smith & D. J. Harrington	1
99	CINET-BASIC	DECUS 8-159	10/21/68	Original	Bud Pembroke and David Gillette	1
100	Demonstration Programs for the PDP-8	DECUS 8-162	Unknown	Original	Various	1
101	Copy	DECUS 8-177	3/1/69	Original	Alexander Symthe	1
102	Poly BASIC	DECUS 8-195	9/16/69	Original	L. Elekman R. Lary	1
103	Evaluating Determinants	DECUS 8-208	6/3/69	Original	A. Moses	1
104	PALH	DECUS 8-212a	3/2/70	Original	Michael Schwake	1
105	Foreground/Background/8 Now	DECUS 8-230	11/24/69	Original	John C. Alderman Jr.	1
106	PEEP – A Directory Search Program	DECUS 8-252	12/23/69	Original	J. M. Dickson	1
107	Two Patches for Disassembler with	DECUS 8-286	2/25/70	Original	Gary Coleman	1
108	LISP-8	DECUS 8-341	12/16/70	Original	William Neal	1
109	Space War	DECUS 8-395	3/22/70	Original	Evan Suits	1
110	Space War Listing	DECUS 8-395	3/22/70	Original	Evan Suits	1
111	MEMO – A Text Formatting Program	DECUS 8-427	5/12/71	Original	Gregory Ruth	1
112	Modifications to the Fixed Point Output in the PDP-8 Floating Point Package (Digital-8-5-S)	DECUS 8-44	Jun-66	Original	A. R. Mackenzie	1
113	FYLHLP – PS/8 File Utility Program	DECUS 8-445	5/10/71	Original	David M. Kristol	1
114	PIPQ	DECUS 8-475	12/30/71	Original	John C. Alderman Jr.	1
115	Monitor Command Extensions in PS/8	DECUS 8-478	1/20/72	Original	John R. Covert	1
116	PS/8 Fortran Alphabetical Sort	DECUS 8-518	2/11/72	Original	Edward Steinfeld	1
117	The PDP-8 Cookbook, Volume 1	DECUS 8-602A	Jan-73	Original	Floor Anthoni	1
118	The PDP-8 Cookbook, Volume 2	DECUS 8-602B	May-73	Original	Floor Anthoni	1
119	INVENT-8	DECUS 8-610	Unknown	Original	Charles Moeder	1
120	Graphing Subroutines for 8K Fortran Programs	DECUS 8-629A	6/14/73	Original	G. Chase	1
121	BEST – Binary to Symbolic Translator	DECUS 8-636	6/22/73	Copy	Michel Morel & Francoise Landre	1
122	DECSYSTEM-8	DECUS 8-646	8/10/73	Original	John R. Covert & Douglas E. Wrege	1
123	Label Program	DECUS 8-68A	3/1/68	Original	Michael S. Wolfberg	1
124	OS/8 Utility Package	DECUS 8-757	3/11/75	Original	A. Windram	1
125	FOCAL8	DEC-08-LFL8A-A-D	Jul-73	Original		2
126	Binary Loader	DEC-08-LBAA-D	5/10/67	Original	Software Services Group	1
127	4K Assemblers PAL III MACRO-8	DEC-08-LAS4A-A-D	Jul-73	Original		1
128	FORTTRAN for the PDP-5/8 – Internal	DEC-05-CFN1-D-(L)	9/14/64	Original	Software Services Group	1

PDP-8 Hardware Documentation

	Product Name	Part Code	Date	Type	Author	No. of Copies
1	PDP-8 Maintenance Manual	F-87A	Jan-71	Original	DEC	1
2	PDP-8 Maintenance Manual	F-87	Feb-66	Original	DEC	1
3	DF32 Disk File and Control Instruction Manual	DEC-D8-IDFA-D	Aug-69	Original	DEC	1
4	DF32 Disk File and Control Instruction Manual	DEC-D8-IDFA-D	Mar-68	Copy	DEC	1
5	DECTape Transport TU55 Instruction Manual	H-TU55	Jan-66	Copy	DEC	1
6	PDP-8 Users Handbook (like new)	F-85	May-66	Original	DEC	1
7	PDP-8 FamilyDF32-D, E Disk File and Control Maintenance Manual	DEC-08-HRDA-D	May-69	Original	DEC	1
8	Technical Manual 33 Teletypewriter Sets Receive Only (RO) Keyboard Send-Receive (KSR) Automatic Send-Receive (ASR)	Bulletin 310 Vol. 1	Sep-68	Original	Teletype Corp	1
9	Technical Manual 33 Teletypewriter Sets Receive Only (RO) Keyboard Send-Receive (KSR) Automatic Send-Receive (ASR)	Bulletin 310 Vol. 2	Sep-68	Original	Teletype Corp	1
10	33 Page Printer Set (ASR, KSR, RO) Parts	Bulletin 11848	Dec-70	Original	Teletype Corp	1
11	Small Computer Handbook		1966	Original	DEC	1
12	Logic Handbook		1968	Original	DEC	1
13	OS/8 Handbook		1974	Original	DEC	1

The following photographs illustrate some of the hardware and software manuals included:

Logic Handbook 1968 Ed.



F-85
5/66

PROGRAMMED DATA
PROCESSOR - 8

PDP-8

USERS HANDBOOK

DIGITAL EQUIPMENT CORPORATION • MAYNARD, MASSACHUSETTS

INSTRUCTION MANUAL

DF32
DISK FILE
AND
CONTROL

DIGITAL EQUIPMENT CORPORATION • MAYNARD, MASSACHUSETTS

H-TU5#

INSTRUCTION MANUAL

Property of CHARLICE
DO NOT REMOVE
from F-24

DECTAPE TRANSPORT
TU55
INSTRUCTION MANUAL

DECTAPE TRANSPORT
TU55

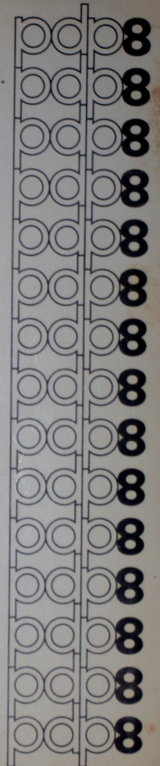
DIGITAL EQUIPMENT CORPORATION • MAYNARD, MASSACHUSETTS

digital

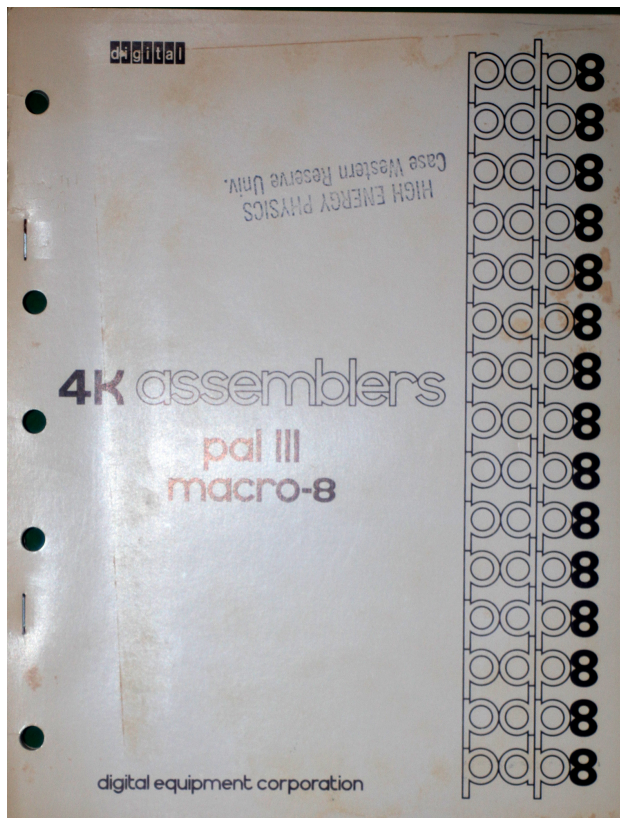
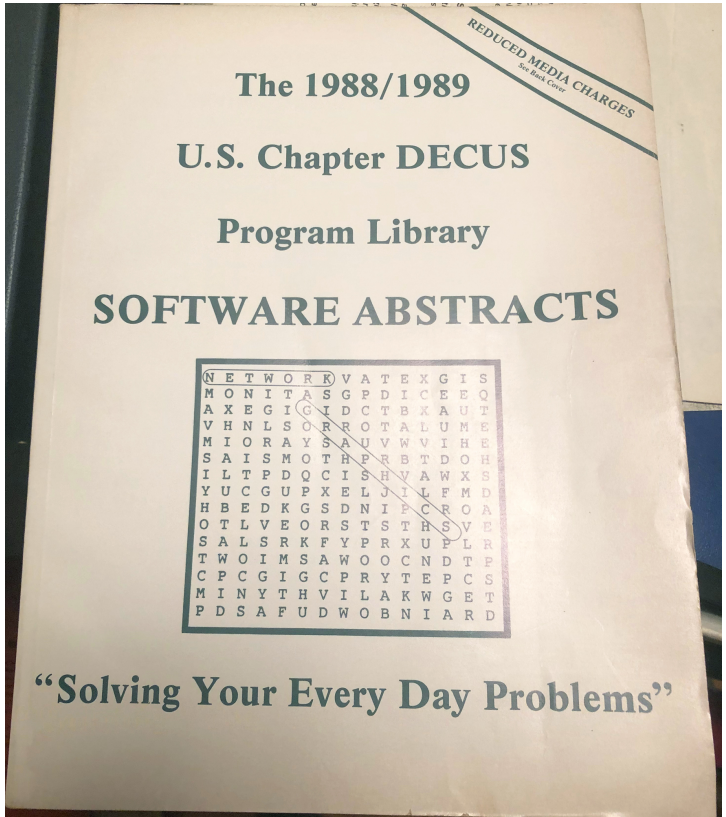
HIGH ENERGY PHYSICS
Case Western Reserve Univ.

focal 8

digital equipment corporation



OS/8 Handbook 1974 Ed.



Engineering Drawings

The following is a list of the engineering drawings that are part of the collection. Drawing numbers, revision levels and revision dates are provided. Some revision dates were illegible and may be listed as “unknown”, in some cases the dates shown may be the dates when the assembly was released to production.

PDP-8 Engineering Drawings

<u>Title</u>	<u>Drawing No.</u>	<u>Rev.</u>	<u>Date</u>
Processor UML	UML-E-8P-0-19	P	3/15/66
B Size (11 x 17) Drawings			
Central Processor PDP-8 Master Drawing List			
PDP-8 Table Model	MA-D-8-0-1	BB	Jun-72
PDP-8 Table Model	MA-D-8-0-1	A	Jul-65 1 of 2
Bus Bar for Power and Logic Wiring	WD-D-8-0-14	A	Jul-65 2 of 2
Accumulator	BS-8P-0-2	D	Jan-69
AC Control	BS-8P-0-3	C	Oct-66
MB Register and Control	BS-8P-0-5	E	Unknown
Major States & Instruction Register	BS-D-8P-0-6	K	Unknown
MA, PC Control	BS-D-8P-0-8	S	Jul-69
Timing, Keys, SWS and Run	BS-D-8P-0-9	J	Jan-70
Input-Output Control	BS-D-8P-0-10	S	Aug-71
Wiring List PDP8-0 (Proc)	WL-8P-0-17	P	Jul-72
Processor UML	UML-E-8P-0-19	AP	Feb-71
Flow Diagram Automatic Operations	FD-D-8P-0-7	W	Unknown
Flow Diagram Manual Operations	FD-D-8P-0-7	C	Feb-66 1 of 2
Indicator Connector for MB Bits	CL-A-8P-0-25	c	Feb-66 2 of 2
Indicator Connector for AC Bits	CL-A-8P-0-26	A	Mar-65
Indicator Connector for MA Bits	CL-A-8P-0-27	A	Mar-65
Indicator Connector for PC & SR Bits	CL-A-8P-0-28	A	Mar-65
Indicator Connector for PA 05	CL-A-8P-0-29	A	Mar-65
PDP-8 Memory Master Drawing List (yellow paper)			
In-Out Buffers (yellow paper)	BS-8M-0-16	AF	Apr-73
Memory UML (yellow paper)	UML-E-8M-0-20	C	Apr-73
PDP-8 Memory Master Drawing List (white paper)	ML-8M-0	S	Apr-73
PDP-8 Table Model	MA-D-8-0-1	AE	Jun-70
PDP-8 Table Model	MA-D-8-0-1	A	Jul-65 1 of 2
Bus Bar for Power and Logic Wiring	WD-D-8-0-14	A	Jul-65 2 of 2
Teletprinter	BS-D-8M-0-11	D	Jan-69
X-Axis Selection	BS-D-8M-0-12	H	Jul-70
Y-Axis Selection	BS-D-8M-0-13	B	Jul-65
Sense Amps, Inhibit Drivers, Mem. Control	BS-D-8M-0-15	B	Jul-65
in-Out Buffers	BS-D-8M-0-16	J	Aug-68
Wiring List PDP8 0 (MEM)	WL-8M-0-18	B	Aug-68
Memory UML	UML-E-8M-0-20	W	Jun-69
AC Bits & IOP's Cable connector	CL-A-8M-0-35	P	Unknown
AC Bits & IOP's Cable connector	CL-A-8M-0-35	B	Aug-68 1 of 2
MB Bits Cable connector	CL-A-8M-0-38	B	Aug-68 2 of 2
MB Bits Cable connector	CL-A-8M-0-38	A	Jun-65 1 of 2
Sense Amps MB Bits Cable connector	CL-A-8M-0-40	A	Jun-65 2 of 2
Sense Amps MB Bits Cable connector	CL-A-8M-0-40	A	Jun-65 1 of 2
Teletprinter Cable connector	CL-A-8M-0-44	A	Jun-65 2 of 2
indicator Connector Cable connector	CL-A-8M-0-39	A	Jun-65
Processor & Memory Chassis connector	CL-A-8-0-30	A	Mar-65
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68 1 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68 2 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68 3 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68 4 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68 5 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68 6 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68 7 of 7
PDP-8 Memory Master Drawing List (yellow paper)			
In-Out Buffers (yellow paper)	BS-8M-0-16	AF	Apr-73
Memory UML (yellow paper)	UML-E-8M-0-20	C	Apr-73
PDP-8 Memory Master Drawing List (white paper)	ML-8M-0	S	Apr-73
PDP-8 Table Model	MA-D-8-0-1	AE	Jun-70
PDP-8 Table Model	MA-D-8-0-1	A	Jul-65 1 of 2
Bus Bar for Power and Logic Wiring	WD-D-8-0-14	A	Jul-65 2 of 2
Teletprinter	BS-D-8M-0-11	D	Jan-69
X-Axis Selection	BS-D-8M-0-12	H	Jul-70
Y-Axis Selection	BS-D-8M-0-13	B	Jul-65
Sense Amps, Inhibit Drivers, Mem. Control	BS-D-8M-0-15	B	Jul-65
in-Out Buffers	BS-D-8M-0-16	J	Aug-68
Wiring List PDP8 0 (MEM)	WL-8M-0-18	B	Aug-68
		W	Jun-69

Memory UML	UML-E-8M-0-20	P	Unknown	
AC Bits & IOP's Cable connector	CL-A-8M-0-35	B	Aug-68	1 of 2
AC Bits & IOP's Cable connector	CL-A-8M-0-35	B	Aug-68	2 of 2
MB Bits Cable connector	CL-A-8M-0-38	A	Jun-65	1 of 2
MB Bits Cable connector	CL-A-8M-0-38	A	Jun-65	2 of 2
Sense Amps MB Bits Cable connector	CL-A-8M-0-40	A	Jun-65	1 of 2
Sense Amps MB Bits Cable connector	CL-A-8M-0-40	A	Jun-65	2 of 2
Teletypewriter Cable connector	CL-A-8M-0-44	A	Jun-65	
indicator Connector Cable connector	CL-A-8M-0-39	A	Mar-65	
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68	1 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68	2 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68	3 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68	4 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68	5 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68	6 of 7
Processor & Memory Chassis connector	CL-A-8-0-30	B	Aug-68	7 of 7

C Size (17 x 22) Drawings

Central Processor PDP-8 Master Drawing List

PDP-8 Table Model	ML-8P-0	BB	Jun-72	
PDP-8 Table Model	MA-D-8-0-1	A	Jul-66	1 of 2
Bus Bar for Power and Logic Wiring	MA-D-8-0-1	A	Jul-66	2 of 2
Accumulator	WD-D-8-0-14	D	Jan-69	
AC Control	BS-8P-0-2	C	Oct-66	
MB Register and Control	BS-8P-0-3	E	Jul-70	
Major States & Instruction Register	BS-8P-0-5	K	7/72(?)	
MA, PC Control	BS-D-8P-0-6	S	Jul-69	
Timing, Keys, SWS and Run	BS-D-8P-0-8	J	Jan-70	
Input-Output Control	BS-D-8P-0-9	S	Feb-71	
Wiring List PDP8-0 (Proc)	BS-D-8P-0-10	P	Jul-72	
Processor UML	WL-8P-0-17	AP	Feb-71	
Flow Diagram Automatic Operations	UML-E-8P-0-19	W	2/72(?)	
Flow Diagram Manual Operations	FD-D-8P-0-7	C	Feb-66	1 of 2
Indicator Connector for MB Bits	FD-D-8P-0-7	c	Feb-66	2 of 2
Indicator Connector for AC Bits	CL-A-8P-0-25	A	Mar-65	
Indicator Connector for MA Bits	CL-A-8P-0-26	A	Mar-65	
Indicator Connector for PC & SR Bits	CL-A-8P-0-27	A	Mar-65	
Indicator Connector for PA 05	CL-A-8P-0-28	A	Mar-65	
	CL-A-8P-0-29	A	Mar-65	

B Size (11 x 17) Drawings

Memory Extension Type 184

Master Drawing List	ML-184-0	L	Apr-70	
Memory Ext. Type 184	MA-D-184-0-1	B	4/2/68	
Sense Amps, Inhibit Drivers, Mem. Cont.	BS-D-184-0-2	L	4/14/70	
Y Axis Selection	BS-D-184-0-3	A	8/23/65	
X Axis Selection	BS-D-184-0-4	A	8/23/65	
184 Memory	WL-184-0-5	H	4/14/70	
Utilization Module List	UML-D184-0-6	D	4/1/70	
184 Memory Buss Schedule	WD-D-184-0-7	A	8/23/65	
External Component List For LINC-8 Extended Memory	CP-184-0-9		4/1/70	

Memory Extension Control Type 183

Master Drawing List	ML-183-0	Z	Aug-68	
PDP-8 Table Model (good, clear photographs)	MA-D-8-0-1	A	7/7/66	1 of 2
PDP-8 Table Model (good, clear photographs)	MA-D-8-0-1	A	7/7/66	2 of 2
MA Buffers and Start Field	BS-D-183-0-2	B	8/27/68	
Memory Extension Control	BS-D-183-0-3	J	9/12/68	
Bus Bar for Power and Logic Wiring	WD-D-0-14	D	1/10/69	
Wiring List PDP 8 0 (Mem)	WL-8M-0-18	W	6/11/69	
Memory UML	UML-E-8M-0-20	R	unknown	
Memory Extension Control In-Out Cable Connector	CL-A-183-0-4		5/7/65	
Buffered MA's Signal Cable Connector	CL-A-183-0-5		5/7/65	1 of 3
Buffered MA's Signal Cable Connector	CL-A-183-0-5		5/7/65	2 of 3
Buffered MA's Signal Cable Connector	CL-A-183-0-5		5/7/65	3 of 3
Indicator Connector MA35	CL-A-183-0-6		5/7/65	
Start Field Signal Cable Connector	CL-A-183-0-7	A	8/27/68	

C Size (17 x 22) (missing approx. 1" from left and right edges)

A405 Sample/Hold

Parts List (2 copies)	PL-A405-0-0	B	2/17/71	1 of 2
Parts List (2 copies)	PL-A405-0-0	B	2/17/71	2 of 2
Sample and Hold A405 (2 copies)	CS-A405-0-1	B	1970	
A405 Tester (Hand Drawn)	CS-A405-0-2		9/28/70	
Test Procedure Sheet	SP-A405-0-3		1/22/70	1 of 2
Test Procedure Sheet	SP-A405-0-3		1/22/70	2 of 2
Assy/Drilling Hole Layout (2 copies)	AH-A405-0-5	B	2/17/71	
Module ECO History	MH-A405-0-6	B	7/20/70	

